

GT40

ROM BOOTSTRAP
MD-11-DDGTD-C

EP-DDGTD-C-DL-B

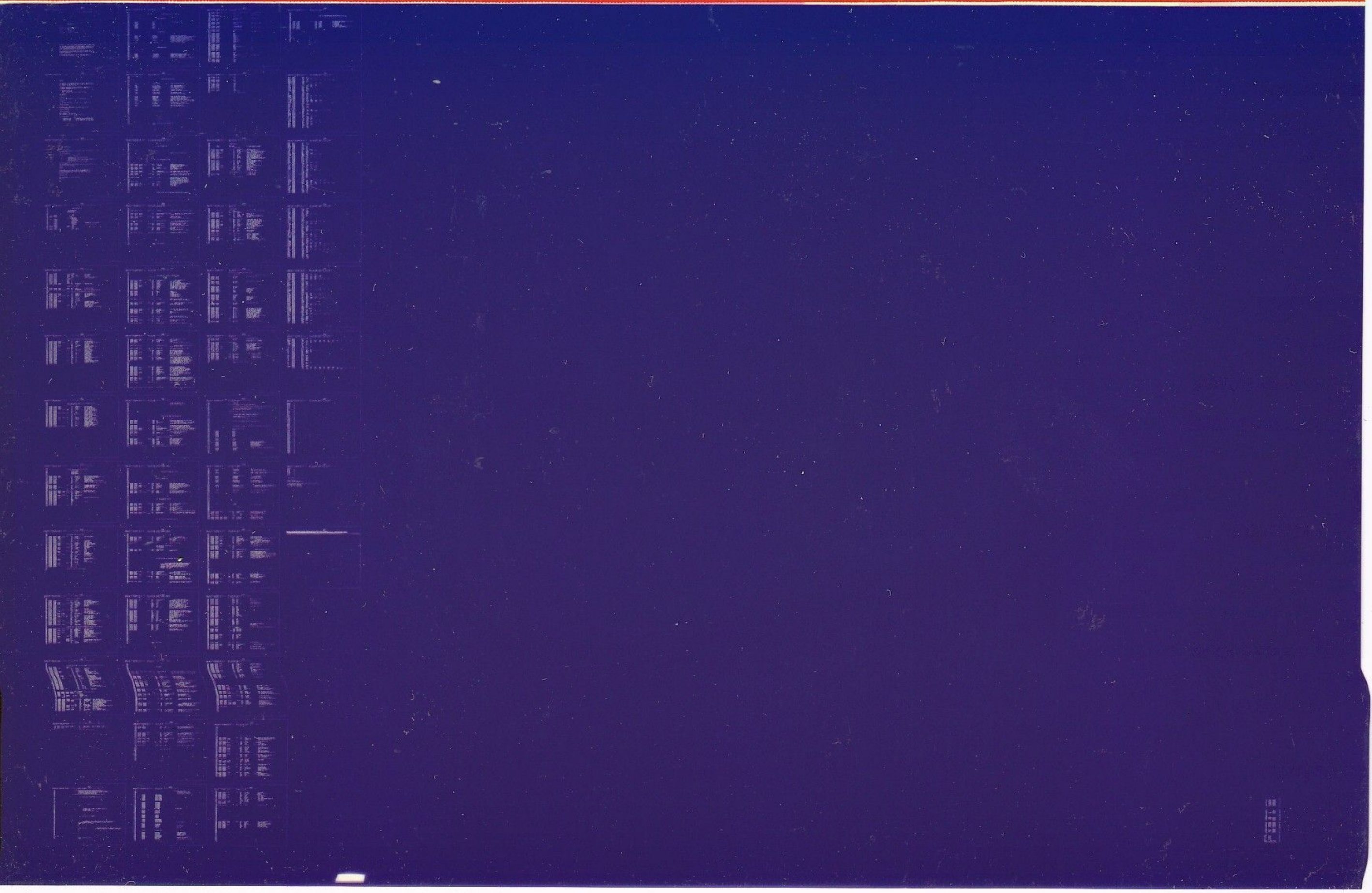
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GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
DDGTD.P1: 15-SEP-76 00:00

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.TITLE GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C

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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DDGTD-C-D
PRODUCT NAME: GT40 ROM VERIFY
DATE: DECEMBER 1976
MAINTAINER: DIAGNOSTIC GROUP

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1. ABSTRACT

 THIS VERSION OF THE PROGRAM SUPPORTS NON-SWITCH REGISTER CPU'S.
 FOR THESE CPU'S, THE SWITCH REGISTER CAN BE CHANGED BY CHANGING
 THE CONTENTS OF SWREG (170).

THE DDGTD-C DIAGNOSTIC PROGRAM IS WRITTEN TO BE USED AS AN AID
 TO HARDWARE DEBUGGING AND MAINTENANCE OF THE GT40 ROM
 BOOTSTRAP LOADER VERSION 1 OR 2.

THE AVAILABLE TESTS ARE
 PRG0 - LOGIC TESTS
 PRG1 - ROM DATA DUMP TO THE CONSOLE TELETYPE

2. REQUIREMENTS

2.1 EQUIPMENT

GT40 DISPLAY PROCESSOR WITH ROM BOOTSTRAP VERSION 1 OR 2.

2.2 STORAGE

THIS PROGRAM USES MEMORY LOCATIONS 0-7776 + 16000-16776(8).

3. LOADING PROCEDURE

 PROCEDURE FOR A NORMAL BINARY TAPE SHOULD BE FOLLOWED.

4. STARTING PROCEDURE

4.1 STARTING ADDRESSES

0200 PROGRAM 0, ROM LOGIC TEST.
 0204 PROGRAM 1, ROM DATA DUMP ON CONSOLE TTY.

4.2 SWITCH SETTINGS

CONSOLE SW 11=0	NORMAL RUN (64. INTERATIONS/TEST)
CONSOLE SW 11=1	SUPPRES SUBPROGRAM INTERATIONS
CONSOLE SW 08=0	TEST AS VERSION 2 ROM (512. WORDS)
CONSOLE SW 08=1	TEST AS VERSION 1 ROM (256. WORDS)

5. PROGRAM DESCRIPTIONS

5.1 PRG0 - LOGIC TESTS

THE LOGIC TESTS CONSIST OF 4 ROUTINES TO TEST THE GT40 ROM
 BOOTSTRAP LOGIC

5.1.1 ROUTINE DESCRIPTIONS

ROUTINE	TESTS
T1	ADDRESSABILITY OF GT40 ROM BOOTSTRAP
T2	DATA RELIABILITY
T3	THAT GT40 ROM BOOTSTRAP TIMES OUT WHEN REFERENCED BY A DATIP BUS CYCLE
T4	THAT DATA READ FROM THE ROM IS CORRECT

5.2 PRG1 - ROM DATA DUMP

THIS PROGRAM TYPES OUT THE 512-256. WORDS OF ROM DATA ON THE
 CONSOLE TELETYPE AND HALTS.

6. ERRORS

THE PROGRAM WILL ONLY HALT ON ERROR. THE PROGRAM DOES NOT
 CONTAIN FACILITIES FOR REPORTING ERROR CONDITIONS.
 TO PLACE THE PROGRAM INTO A SCOPE LOOP, REPLACE THE ERROR
 HALT WITH A NOP.

7. EXECUTION TIME

PRG0 TAKES APPROX. 5 SECONDS PER PASS.
 PRG1 N/A
 PRG2 N/A

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000034 000034
000036 002024
000036 000000
104400
177564
177566
177776
177570
000004
000500
000170
000170 000000
000172 177570
000200 000200
000204 000137 001024
000204 000137 001500
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```
.LIST ME,BIN,SEQ,LD
.ENABL ABS,AMA

:LOAD ADDRESS=0200
:DEPRESS START
:STACK POINTER IS AT 500
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```
.LIST
.=34
SCOPEC
0
:EQUATE STATEMENTS
SCOPE=TRAP
TPCSR=177564
TPDBR=177566
PSW=177776
DSWR=177570
ERRVEC=4
STKPTR=500
.=170
SWREG: .WORD 0
SWR: DSWR
.=200
JMP PRMTRS
JMP PRG1
```

```
;ADDRESS OF SWITCH REGISTER
;INITIAL STACK SETTING
```

F01

GT4C ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
DDGTDG.P11 15-SEP-76 00:00

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167
168          001000      001000
169          001000      166000      ROMADD: 166000      ;ROM ADDRESS
170          001002      001000      WORDS: 512.      :256.      ;ROM LENGTH
171          001004      006000      IMAGE: START      :STARTA      ;ROM IMAGE
172          001006      172002      DSR: 172002      ;DISPLAY STATUS REGISTER
173          001010      000010      FILLER: 10      ;# OF FILLER CHAR
174          001012      000010      FILCNT: 10
175          001014      000000      ICNT: 0
176          001016      000000      DUMP: 0
177          001020      000000      CHARA: 0
178          001022      000000      TERM: 0
179          001024      012706      000500      PRMTRS: MOV      #STKPTR,%6      ;SET STACK PTR
180          001030      004737      002256      JSR      PC,SWITCH      ;CHECK ROM VERSION
181
182          ;PROGRAM 0 LOGIC TESTS
183
184          001034      005037      001014      PRGD: CLR      ICNT      ;CLEAR PASS COUNT
185          001040      012706      000500      PRGOR: MOV      #STKPTR,%6
186          001044      012737      001040      002100      MOV      #PRGOR,RETURN      ;SET RETURN ADDRESS FOR SCOPE
187
188          ;TEST1 TEST ABILITY TO REFERENCE ROM WITHOUT TIMING OUT
189
190          001052      013700      001000      T1: MOV      ROMADD,%0      ;GET ROM ADDRESS
191          001056      013701      001002      MOV      WORDS,%1      ;GET ADDRESS COUNTER
192          001062      012737      001122      000004      MOV      #ERROR1,4      ;SET UP TIME OUT VECTOR
193          001070      011003      T1A: MOV      (0),%3      ;REFERENCE
194          001072      005720      TST      (0)+      ;ROM
195          001074      064037      001016      ADD      -(0),DUMP
196          001100      021010      CMP      (0),(0)
197          001102      132020      BITB      (0)+,(0)+
198          001104      164037      001016      SUB      -(0),DUMP
199          001110      062700      000002      ADD      #2,%0      ;INCREMENT POINTER
200          001114      005301      DEC      %1      ;DECREMENT ADDRESS COUNTER
201          001116      001364      BNE      T1A      ;BRANCH IF NOT FINISHED
202          001120      000403      BR      T1B      ;GO TO SCOPE LOOP
203          001122      022626      ERROR1: CMP      (6)+,(6)+      ;REPOSITION STACK
204          001124      000000      HALT
205          001126      000760      BR      T1A      ;ERROR, TIME-OUT ON ROM ADDRESS
206          001130      104400      T1B: SCOPE
207

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GO1

GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
 DCGTDC.P11 15-SEP-76 00:00

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208
209          :TEST2 TEST THAT ROM DATA CAN BE READ RELIABLY.
210
211 001132 013700 001000          T2:  MOV  ROMADD,%0          ;GET ROM ADDRESS
212 001136 013701 001002          MOV  WORDS,%1          ;GET ADDRESS COUNTER
213 001142 012737 000006 000004 T2A: MOV  #6,4          ;INITIALIZE TIME OUT VECTOR
214 001150 005037 001016          CLR  DUMP              ;INITIALIZE DUMP
215 001154 011003          MOV  (0),%3           ;GET DATA
216 001156 062037 001016          ADD  (0)+,DUMP        ;ADD DATA TO DUMP
217 001162 163703 001016          SUB  DUMP,%3          ;SUBTRACT DATA FROM DATA
218 001166 001402          BEQ  T2B              ;BRANCH IF EQUAL
219 001170 000000          ERROR2: HALT          ;DATA ERROR
220 001172 000766          BR   T2A              ;LOOP ON ERROR
221 001174 044037 001016          T2B: BIC  -(0),DUMP    ;CLEAR DUMP BITS
222 001200 001402          BEQ  T2C              ;BRANCH IF EQUAL TO C
223 001202 000000          HALT                  ;DATA ERROR
224 001204 000773          BR   T2B              ;LOOP ON ERROR
225 001206 021010          T2C: CMP  (0),(0)      ;COMPARE DATA
226 001210 001402          BEQ  T2D              ;BRANCH IF EQUAL
227 001212 000000          HALT                  ;DATA ERROR
228 001214 000774          BR   T2C              ;LOOP ON ERROR
229 001216 122040          T2D: CMPB (0)+,-(0)   ;COMPARE DATA (BYTE OPERATION)
230 001220 001402          BEQ  T2E              ;BRANCH IF EQJAL
231 001222 000000          HALT                  ;DATA ERROR
232 001224 000774          BR   T2D              ;LOOP ON ERROR
233 001226 005720          T2E: TST  (0)+        ;INCREMENT ADDRESS POINTER
234 001230 005301          DEC  %1               ;DECREMENT ADDRESS COUNTER
235 001232 001346          BNE  T2A              ;RETURN IF NOT DONE
236 001234 104400          SCOPE
237

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H01

GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
DDGTDC.P11 15-SEP-76 00:00

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238
239
240
241
242 001236 012706 000500 T3: MOV #STKPTR,%6 ;SET STACK PTR
243 001242 013700 001000 MOV ROMADD,%0 ;GET ROM ADDRESS
244 001246 013701 001002 MOV WORDS,%1 ;GET ADDRESS COUNTER
245 001252 012737 001266 000004 T3AA: MOV #T3B,4 ;SET UP TIME OUT VECTOR
246 001260 010010 T3A: MOV %0,(0) ;ATTEMPT TO ALTER DATA
247 001262 000000 HALT ;HERE IF DID NOT TIME OUT
248 001264 000775 BR T3A ;LOOP ON ERROR
249 001266 012737 001304 000004 T3B: MOV #T3D,4 ;SET UP TIME OUT VECTOR
250 001274 022626 T3C: CMP (6)+,(6)+ ;REPOSITION STACK
251 001276 005210 T3C: INC (0) ;ATTEMPT TO ALTER DATA
252 001300 000000 HALT ;HERE IF DID NOT TIME OUT
253 001302 000775 BR T3C ;LOOP ON ERROR
254 001304 012737 001324 000004 T3D: MOV #T3F,4 ;SET UP TIME OUT VECTOR
255 001312 022626 T3D: CMP (6)+,(6)+ ;REPOSITION STACK
256 001314 005077 177460 T3E: CLR @ROMADD ;ATTEMPT TO ALTER DATA
257 001320 000000 HALT ;HERE IF DID NOT TIME OUT
258 001322 000774 BR T3E ;LOOP ON ERROR
259 001324 005720 T3F: TST (0)+ ;INCREMENT ADDRESS POINTER
260 001326 022626 T3F: CMP (6)+,(6)+ ;REPOSITION STACK
261 001330 005301 DEC %1 ;DECREMENT ADDRESS COUNTER
262 001332 001347 BNE T3AA ;RETURN IF NOT DONE
263 001334 012737 000006 000004 MOV #6,@#4 ;RESTORE TIME OUT TRAP
264 001342 104400 SCOPE ;SCOPE LOOP

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I01

GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
DDGTDC.P11 15-SEP-76 00:00

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265
266          :COMPARE THE ROM DATA TO THE IMAGE DATA
267          :      RO=WORD NUMBER
268          :      R1=GOOD ADDRESS
269          :      R2=GOOD DATA
270          :      R3=BAD ADDRESS
271          :      R4=BAD DATA
272
273 001344 012700 000000 T4:   MOV    #0,%0          ;SET UP INITIAL WORD COUNT
274 001350 013701 001004      MOV    IMAGE,%1        ;SET UP STARTING ADDRESS OF ROM IMAGE
275 001354 013703 001000      MOV    ROMADD,%3       ;SET UP STARTING ROM ADDRESS
276 001360 011102 T4A:  MOV    (%1),%2        ;READ EXPECTED VALUE
277 001362 011304      MOV    (%3),%4        ;READ ROM VALUE
278 001364 020204      CMP    %2,%4          ;COMPARE EXPECTED TO THE VALUE READ
279 001366 001402      BEQ   T4B             ;BRANCH IF CORRECT
280 001370 000000      HALT
281 001372 000772      BR    T4A            ;ERROR, ROM VALUE FAILED TO EQUAL EXPECTED
282
283 001374 022123 T4B:  CMP    (%1)+,(%3)+    ;INCREMENT ADDRESSES POINTERS
284 001376 005200      INC    %0             ;INCREMENT WORD COUNT
285 001400 023700 001002      CMP    WORDS,%0       ;COMPARE IF END WORD
286 001404 001365      BNE   T4A            ;BRANCH IF NOT LAST WORD
287 001406 104400 T4E:  SCOPE
288
289 001410 005237 001014 END:   INC    ICNT          ;INCREMENT PASS COUNT
290 001414 012777 000001 177364      MOV    #1,%DSR        ;RING THE GT40 BELL
291 001422 012737 000207 177566 DONEY: MOV    #207,%TPDBR   ;RING THE TELETYPE BELL
292 001430 105737 177564      TSTB  %TPCSR
293 001434 100375      BPL   .-4
294 001436 012737 000207 177566      MOV    #207,%TPDBR
295 001444 105737 177564 1S:   TSTB  %TPCSR
296 001450 100375      BPL   1S
297 001452 013700 000042      MOV    %42,%0        ;RETURN TO DECTAPE MONITOR?
298 001456 001406      BEQ   DONE1
299 001460 000005      RESET
300 001462 000005      RESET
301 001464 004710      JSR   7,(0)          ;RETURN!
302 001466 000240      NOP
303 001470 000240      NOP
304 001472 000240      NOP
305 001474 000137 001034 DONE1: JMP    PRGO
306

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307
308           :THIS PROGRAM TYPES OUT ROM DATA
309
310 001500 012706 000500          PRG1:  MOV  #STKPTR,%E      ;INITIALIZE STACK
311 001504 012737 000006 000004  MOV  #6,%4          ;SET UP BUSS ERROR
312 001512 004737 002256          JSR  PC SWITCH
313 001516 004537 001662          JSR  5,TYPEM
314 001522 002245          MB
315 001524 004537 001662          JSR  5,TYPEM      ;TYPE MESSAGE
316 001530 002222          M7          ;'ROM DATA'
317 001532 0137C1 001002          MOV  WORDS,%1      ;GET # OF WORDS
318 001536 013700 001000          PRG1A: MOV  ROMADD,%0  ;GET STARTING ADDRESS
319 001542 012702 000010          MOV  #10,%2       ;GET ADDRESS INDICATOR
320 001546 105737 177564          TSTB TPCSR        ;WAIT FOR
321 001552 100375          BPL  .-4          ;TELEPRINTER FLAG
322 001554 010037 002114          PRG1B: MOV  %0,D2BTYP ;GET ADDRESS
323 001560 004737 002116          JSR  7,02A       ;AND TYPE IT
324 001564 004537 001662          JSR  5,TYPEM     ;TYPE
325 001570 002251          M9          ;CR/LF
326 001572 012037 002114          PRG1C: MOV  (0)+,D2BTYP ;TYPE
327 001576 004737 002116          JSR  7,02A       ;DATA
328 001602 105737 177564          TSTB TPCSR        ;WAIT FOR
329 001606 100375          BPL  .-4          ;TELEPRINTER FLAG
330 001610 012737 000040 177566  MOV  #',TPDBR     ;TYPE SPACE
331 001616 005301          DEC  %1          ;ALL DATA TYPED
332 001620 001410          BEQ  PRG1D       ;GO TO FINISH
333 001622 005302          DEC  %2
334 001624 001362          BNE  PRG1C       ;RETURN TO PRG1B
335 001626 012702 000010          MOV  #10,%2      ;GET ADDRESS INDICATOR
336 001632 004537 001662          JSR  5,TYPEM     ;TYPE
337 001636 002245          MB          ;CR/LF
338 001640 000745          BR   PRG1B       ;RETURN TO PRG1B
339 001642 004537 001662          PRG1D: JSR  5,TYPEM
340 001646 002245          MB
341 001650 004537 001662          JSR  5,TYPEM
342 001654 002245          MB
343 001656 000000          HALT
344 001660 000707          BR   PRG1
345
346           ;ROUTINE TO LOOP ON A SINGLE ADDRESS
347

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L01

GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
DDGTD.C.P11 15-SEP-76 00:00

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400
401      ;THIS ROUTINE CONVERTS AN OCTAL NUMBER TO ASCII AND TYPES IT ON THE TTY.
402
403      D2BTYP: 0
404      002114 000000      177564      O2A:  MOV  TPCSR, -(6)      ;SAVE TPCSR
405      002116 013746      MOV  %2, -(6)      ;SAVE R2
406      002122 010246      MOV  %1, -(6)      ;SAVE R1
407      002124 010146      MOV  %0, -(6)      ;SAVE R0
408      002126 010046      MOV  D2BTYP, %0      ;GET DATA TO BE TYPED
409      002130 013700 002114      MOV  #6, %1      ;GET COUNTER
410      002134 012701 000006      CLR  %2      ;CLEAR WORKING REGISTER
411      002140 005002      ROL  %0      ;MOV FIRST BIT (MSB) INTO
412      002142 006100      ROL  %2      ;R2
413      002144 006102      O2AA: ADD  #260, %2      ;FORM ASCII CODE
414      002146 062702 000260      TSTB TPCSR      ;TEST TELEPRINTER
415      002152 105737 177564      BPL  -4      ;FLAG AND WAIT UNTIL DONE
416      002156 100375      MOV  %2, TPDBR      ;LOAD TELEPRINTER BUFFER
417      002160 010237 177566      CLR  %2      ;CLEAR WORKING REGISTER
418      002164 005002      ROL  %0      ;ROTATE THE
419      002166 006100      ROL  %2      ;NEXT
420      002170 006102      ROL  %0      ;OCTAL CHARACTER
421      002172 006100      ROL  %2      ;INTO
422      002174 006102      ROL  %0      ;REGISTER
423      002176 006100      ROL  %2      ;TWO
424      002200 006102      DEC  %1      ;DECREMENT COUNTER
425      002202 005301      BNE  O2AA      ;GO TO O2AA IF NOT 0
426      002204 001360      MOV  (6)+, %0      ;FINISHED. RESTORE REGISTERS
427      002206 012600      MOV  (6)+, %1
428      002210 012601      MOV  (6)+, %2
429      002212 012602      MOV  (6)+, TPCSR      ;AND TPCSR
430      002214 012637 177564      RTS  7      ;AND EXIT
431
432      ;ASCII MESSAGES
433      002222 022500 052107 032055      M7:  .ASCII  'a%GT-40 ROM DATA%'
434      002230 020060 047522 020115
435      002236 040504 040524 022445
436      002244      100
437      002245      100 022445      100      M8:  .ASCII  'a%'
438      002251      100 020040      100      M9:  .ASCII  'a a'
439      002256      .EVEN
440
441      002256 013746 000004      SWITCH: MOV  @#ERRVEC, -(SP)      ;SAVE VECTORS CONTENTS
442      002262 012737 002310 000004      MOV  #1$, @#ERRVEC      ;SET UP FOR TRAP
443      002270 012737 177570 000172      MOV  #DSWR, @#SWR      ;SET UP TO TEST FOR SWITCH REGISTER
444      002276 022777 177777 175666      CMP  #-1, @#SWR      ;TEST FOR SWITCH REGISTER
445      002304 001005      BNE  3$      ;SWITCH REGISTER IS PRESENT
446      002306 000401      BR   2$      ;NO SWITCH REGISTER
447      002310 022626      1$:  CMP  (SP)+, (SP)+      ;POP 2 WORDS OFF STACK
448      002312 012737 000170 000172      2$:  MOV  #SWREG, @#SWR      ;SET UP FOR SOFTWARE SWITCH REGISTER
449      002320 012637 000004      3$:  MOV  (SP)+, @#ERRVEC      ;RESTORE VECTORS CONTENTS
450      002324 032777 000400 175640      BIT  #400, @#SWR      ;TEST BIT 8
451      002332 001007      BNE  4$      ;BR IF VERSION 1
452      002334 012737 001000 001002      MOV  #512, WORDS      ;SET UP VERSION 2 LENGTH
453      002342 012737 006000 001004      MOV  #START, IMAGE      ;SET UP VERSION 2 STARTING ADD.
454      002350 000406

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MO1

GT40 ROM BOOTSTRAP TEST MAINDEC-11-DDGTD-C
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455 002352 012737 000400 001002 4\$:
456 002360 012737 01E000 001004
457 002366 000207 5\$:
458
459

MOV #256.WORDS ;SET UP VERSION 1 LENGTH
MOV #STARTA.IMAGE ;SET UP VERSION : STARTING ADDR.
RTS PC
.SBTTL ROM VERSION 2 VALUES

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EXCEPT FOR THE NEW ORGIN ADDRESS AND SEVERAL "160000"
FOR ADDRESS FUDGING THIS IS AN EXACT COPY OF THE CONTENTS
OF THE GT-40 BOOTSTRAP VERSION #2

.TITLE SCROLLING ROM BOOTSTRAP FOR THE GT40

; BOOTGT.T16 OCT 10, 1973

; COPYRIGHT 1973, DIGITAL EQUIPMENT CORPORATION
; 146 MAIN STREET
; MAYNARD, MASSACHUSETTS 01754

; WRITTEN BY JACK BURNES.

; THIS PROGRAM IS THE SECOND VERSION THE THE ROM BOOTSTRAP FOR
; THE GT40 DISPLAY TERMINAL. IT INCLUDES SCROLLING AND AN END OF
; MEMORY SEARCH FOR THE LOADER.

.ENABL ABS.AMA ;ASSEMBLER DIRECTIVES FOR ABSOLUTE BINARY OUTPUT
; NOTE: USE "MACDLX" TO ASSEMBLE THIS PROGRAM.

.SBTTL DEFINITION SECTION
.PAGE

REGISTER DEFINITIONS

BASIC DEFINITIONS

000000	R0=%0	:DEFINE STANDARD VALUES.
000001	R1=%1	
000002	R2=%2	
000003	R3=%3	
000004	R4=%4	
000005	R5=%5	
000006	SP=%6	
000007	PC=%7	

GT40 DEFINITIONS

000000	CHAR=R0	:CONTAINS THE INPUT CHARACTER.
000001	POINTR=R1	:POINTS TO NEXT INSEPTION BYTE IN DISPLAY BUFFER
000002	TABCNT=R2	:CHARACTER COUNTER FOR THE "TAB" FEATURE.
000003	SCAN=R3	:GENERALLY CONTAINS A POINTER WHICH
		:IS USED WHEN SCANNING MEMORY FOR SOMETHING.
000004	HOLD=R4	:TYPICALLY A TEMPORARY WHICH IS USED TO RETAIN
		:A VALUE FOR A SHORT TIME.
000005	COUNTR=R5	:TYPICALLY USED AS A COUNTER.

LOADER DEFINITIONS

000000	L.BYT=CHAR	:CHARACTER INPUT FOR THE LOADER.
000001	L.ADR=POINTR	:CURRENT MEMORY ADDRESS TO BE LOADED.
000002	L.BC=TABCNT	:NUMBER OF DATA ITEMS TO LOAD.
000005	L.CKSM=COUNTR	:CHECKSUM ON THE INPUT DATA.
000003	INDEX=SCAN	:INDICATES HOW TO ASSEMBLE THE 8 BIT CHARACTER.

000000-000007
000008-00000F
000010-00001F
000020-00002F
000030-00003F
000040-00004F
000050-00005F
000060-00006F
000070-00007F
000080-00008F
000090-00009F
0000A0-0000AF
0000B0-0000BF
0000C0-0000CF
0000D0-0000DF
0000E0-0000EF
0000F0-0000FF

MAJOR SYSTEM DEFINITIONS

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166000          ORIGIN=166000          :ORIGIN OF THE BOOTSTRAP.

175610          DL11IS=175610          :INPUT STATUS REGISTER OF DL11
175612          DL11IB=DL11IS+2        :INPUT CHARACTER FROM DL11
175614          DL11OS=DL11IB+2        :OUTPUT STATUS OF THE DL11
175616          DL11OB=DL11OS+2        :OUTPUT CHARACTER TO THE DL11

177560          KBDIS=177560          :KEYBOARD INPUT STATUS
177562          KBDIB=KBDIS+2          :CURRENT CHARACTER FROM KEYBOARD.

172000          GT40PC=172000          :GT40 PROGRAM COUNTER.
:72002          GT40SR=GT40PC+2        :GT40 STATUS REGISTER ADDRESS.

001000          BSTART=1000           :START OF THE DISPLAY BUFFER
007000          BLIMIT=7000           :APPROXIMATE END OF THE DISPLAY BUFFER.
007776          TMPEND=7776           :LOCATION OF INITIALIZATION STACK.
000004          CORSTR=4              :LOCATION OF PDP-11 TRAP VECTOR.
007012          JMPADD=BLIMIT+10.     :WHERE THE POINTER IS TO FIRST CHAR ON SCREEN
000040          NUMLIN=32             :NUMBER OF LINES ON TEXT TO SHOW ON THE SCREEN

005015          CRLF=5015             :CARRIAGE RETURN - LINE FEED
000175          ALTMOD=175            :THE "KEY" CHARACTER [I.E. ALTMODE].

160000          DISJMP=160000         :THE GT40 JMP INSTRUCTION
:73000          DISTOP=173000         :THE GT40 STOP DISPLAY INSTRUCTION.

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GT40 BOOTSTRAP CODE

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006000      . = 6000
:           . = ORIGIN           ; DEFINE ORIGIN OF THE BOOTSTRAP.
```

COLD INITIALIZATION CODE

```
006000 000005      START: RESET           ; RESET ALL HARDWARE NOW.
006002 012737 000007 175610  MOV      #7, DL11IS          ; INITIALIZE DL-11 INPUT NOW.
006010 012706 007776      MOV      #TMPEND, SP        ; ESTABLISH A GOOD TEMPORARY STACK
:                                           ; POINTER FOR CORE SEARCH.
006014 005237 175614      INC      DL110S          ; SET BREAK BIT
006020 004337 166652      JSR     SCAN, OUTLIT!160000 ; FOR 2 CHARACTER TIMES
006024 000000      .WORD 0          ; SEND TWO ZERO'S

006026 012703 000004      MOV      #CORSTR, SCAN        ; GET ADDRESS OF BAD CORE TRAP VECTOR.
006032 012723 166042      MOV      #NOTHERE!160000, (SCAN)+ ; AND INSERT A POINTER TO US THERE.

006036 005023      ENDCOR: CLR      (SCAN)+          ; NOW CLEAR ALL OF MEMORY BEYOND THE POINTER.
006040 000776      BR       ENDCOR          ; UNTIL WE RUN OUT OF MEMORY AND TRAP.

006042 005743      NOTHER: TST     -(SCAN)          ; WHEN WE TRAP OUT, WE COME HERE.
:                                           ; WE BACK UP POINTER TO GOOD CORE.
:                                           ; NOTE THAT IF WE TRAP OUT AGAIN, IT
:                                           ; IS STILL OK, BECAUSE WE WILL LOOP
:                                           ; UNTIL WE GET A GOOD CORE ADDRESS.
006044 010306      MOV      SCAN, SP          ; WHEN WE GET ONE, THAT IS LAST LOCATION
:                                           ; IN THE MACHINE, AND HENCE OUR SP.
006046 105737 175614      IS:   TSTB   DL110S          ; SEE IF BREAK IS DONE
006052 100375      BPL     IS              ; NO GO BACK
006054 005037 175614      CLR     DL110S          ; CLEAR BREAK BIT
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RESTART INITIALIZATION CODE WHEN COMMUNICATIONS IS WORKING.

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006060 052706 007776

006064 012703 006700
006070 012702 000040

006074 012723 005015
006100 005302
006102 003374

006104 012703 166432

006110 012302
006112 001405
006114 012301

006116 012321
006120 005302
006122 003375
006124 000771

006126 012701 006776

RESTR: BIS #TMPEND,SP

MOV #BLIMIT-NUMLIN-NUMLIN,SCAN
MOV #NUMLIN,TABCNT

SETLP1: MOV #CRLF,(SCAN)+
DEC TABCNT
BGT SETLP1

MOV #SETUP!160000,SCAN

SETLP2: MOV (SCAN)+,TABCNT
BEQ SETDUN
MOV (SCAN)+,POINTR

SETLP3: MOV (SCAN)+,(POINTR)+
DEC TABCNT
BGT SETLP3
BR SETLP2

SETDUN: MOV #BLIMIT-2,POINTR

:FORCE THE SP TO LIMIT OF EXISTING CORE.

:NOW WE WILL FILL THE KEY AREAS OF THE
:DISPLAY BUFFER WITH INITIAL CR-LF'S.

:INSERT A CRLF NOW.
:AND LOOP UNTIL DONE.
:THUS DISPLAY CORE IS ALMOST CORRECT.

:NOW WE WILL INITIALIZE CORE FOR THE
:DISPLAY. PICK UP POINTER TO LIST.

:GET NUMBER OF ITEMS TO INSERT.
:IF ZERO, WE ARE DONE.
:PICK UP FIRST CORE ADDRESS POINTER.

:MOVE OVER A DATA ITEM NOW.
:ALL DONE?
:NOPE. MOVE OVER THE NEXT.
:YES. GET NEXT MAJOR LIST TO INSERT.

:ESTABLISH THE BUFFER POINTER NOW.


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726 006132 004737 166564      NXTCHR: JSR    PC.GETCHR!160000      ;GET A CHARACTER NOW.
727 006136 020027 000177      CMP    CHAR,#177      ;IS IT OUT OF RANGE?
728 006142 002373      BGE    NXTCHR      ;YEP. GET ANOTHER ONE.
729 006144 020027 000040      CMP    CHAR,#40      ;IS IT A PRINTING CHARACTER?
730 006150 002020      BGE    NORMAL      ;YES. IT'S A NORMAL PRINTING CHARACTER.
731 006152 010003      MOV    CHAR,SCAN      ;MOVE IT OVER SO WE CAN PLAY WITH IT.
732 006154 162703 000007      SUB    #7,SCAN      ;BIAS SO THAT BELL [7] IS ZERO.
733 006160 020327 000007      CMP    SCAN,#7      ;IF CHARACTER IS LESS THEN BELL OR
734 006164 103362      BHS    NXTCHR      ;GREATER THEN CR, THEN IGNORE.
735 006166 006303      ASL    SCAN      ;IF GOOD, MAKE IT WORD INDEX.
736 006170 060307      ADD    SCAN,PC      ;AND GO TO THE CORRECT ROUTINE.
737
738 006172 000426      BR     BELL      ;7=BELL
739 006174 000406      BR     NORMAL    ;10=BACKSPACE
740 006176 000411      BR     TAB       ;11=TAB
741 006200 000437      BR     LF        ;12=LINE FEED [LF]
742 006202 000420      BR     VT        ;13=VERTICAL TAB [VT]
743 006204 000424      BR     FF        ;14=FORM FEED [FF]
744
745
746
747 006206 012702 177777      CR:    MOV    #-1,TABCNT      ;RESET TAB POSITION ON A CR, AND
748                                     ;FALL THROUGH TO INSERT THE CHARACTER.
749
750
751 006212 004737 166350      NORMAL: JSR    PC.INSERT!160000     ;INSERT THE CHARACTER IN THE BUFFER.
752 006216 005202      INC    TABCNT      ;UPDATE TAB POSITION NOW.
753 006220 000744      BR     NXTCHR     ;AND GET NEXT CHARACTER.
754
755
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757
758 006222 012700 000040      TAB:   MOV    #40,CHAR      ;ON A TAB, INSERT BLANKS UNTIL THE
759 006226 004737 166350      JSR    PC.INSERT!160000     ;NEXT CHARACTER POSITION IS A MULTIPLE
760 006232 005202      INC    TABCNT      ;OF 8.
761 006234 032702 000007      BIT    #7,TABCNT      ;ARE WE DONE YET?
762 006240 001370      BNE    TAB        ;NOPE.
763 006242 000733      BR     NXTCHR     ;YES.
764
765
766 006244 111705      VT:    MOVB   (PC),COUNTR    ;THIS PUTS THE LOW BYTE OF THE
767                                     ;BRANCH CODE IN COUNTR-SAVE A WORD
768 006246 000405      BR     FFLOOP
769
770 006250 005037 172002      BELL:  CLR    GT4OSR      ;RING BELL -WRITE IN GT4OSR
771 006254 000726      BR     NXTCHR     ;AND LOOP BACK
772
773 006256 012705 000040      FF:    MOV    #NUMLIN,COUNTR ;FORM FEED IS DONE BY INSERTING LF'S.

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774
775 006262 012700 000012      FFLOOP: MOV      #12,CHAR      ;MAKE THE CHARACTER A LINEFEED.
776 006266 004737 166304      JSR      PC,LFSUB!160000      ;DO A LINEFEED.
777 006272 005305              DEC      COUNTR              ;DONE?
778 006274 003372              BGT      FFLOOP              ;NOPE. KEEP SENDING THEM.
779 006276 000715              BR       NXTCHR              ;YES. NOW RETURN. DO NOT FALL THROUGH.
780
781
782 006300 012746 166132      LF:      MOV      #NXTCHR!160000,-(SP)      ;RETURN TO NXTCHR AFTER PROCESSING
783                                     ;THE LF BY FAKING A JSR.
784
785 006304 013703 007012      LFSUB:  MOV      JMPADD,SCAN      ;GET POINTER TO FIRST CHAR ON SCREEN
786
787 006310 122300              LFLOOP: CMPB     (SCAN)+,CHAR      ;AND LOOK FOR A LINEFEED.
788 006312 001406              BEQ      LFOUND              ;GOT IT. SEARCH HAS ENDED.
789 006314 020327 007000      CMP      SCAN,#BLIMIT        ;ARE WE AT END OF BUFFER?
790 006320 103773              BLO      LFLOOP              ;NOPE. KEEP ON LOOKING.
791 006322 012703 001000      MOV      #BSTART,SCAN        ;IF AT TOP, RESET TO BOTTOM OF BUFFER
792 006326 000770              BR       LFLOOP              ;AND KEEP ON LOOKING.
793
794 006330 005203              LFOUND: INC      SCAN          ;WE'VE GOT THE LINE FEED. STOP SHOWING
795 006332 042703 000001      BIC      #1,SCAN            ;FIRST LINE BY CHANGING THE "DISJMP"
796 006336 010337 007012      MOV      SCAN,JMPADD        ;INSTRUCTION TO FIRST CHAR BEYOND LF.
797 006342 004737 166350      JSR      PC,INSERT!160000    ;INSERT THE LF IN THE BUFFER.
798 006346 005000              CLR      CHAR                ;AND THEN INSERT ONE NULL CHARACTER BECAUSE
799                                     ;THE "DISJMP" ADDRESS MUST BE EVEN, AND
800                                     ;THIS GUARANTEES WE WILL NOT LOSE A
801                                     ;A GOOD DATA CHARACTER. WE FALL THROUGH
802                                     ;TO INSERT THE NULL IN THE BUFFER.
803
804
805 006350 110021              INSERT: MOVB     CHAR,(POINTR)+    ;STICK IN THE CHARACTER NOW.
806 006352 032701 000001      BIT      #1,POINTR          ;IS NEXT POSITION EVEN OR ODD?
807 006356 001021              BNE      INSRTX              ;ODD. NO PROBLEMS. SPACE IS ALLOCATED.
808 006360 020127 007000      CMP      POINTR,#BLIMIT      ;EVEN. ARE WE AT THE END OF THE BUFFER?
809 006364 103410              BLO      INSRTL              ;NO. JUST MAKE ROOM FOR ANOTHER WORD.
810 006366 010103              MOV      POINTR,SCAN        ;AT THE END. MOVE THE STUFF TO THE
811 006370 012701 001000      MOV      #BSTART,POINTR      ;BEGINNING OF THE BUFFER.
812 006374 004737 166406      JSR      PC,INSRTL!160000    ;CALL THE ROUTINE TO SAVE SPACE.
813 006400 005023              CLR      (SCAN)+            ;AND CLEAR UP THE INSTRUCTIONS AT THE
814 006402 005013              CLR      (SCAN)             ;END OF THE BUFFER.
815 006404 000207              RTS      PC                  ;AND THEN RETURN.
816
817 006406 022121              INSRTL: CMP      (POINTR)+,(POINTR)+    ;BYPASS THE "DISJMP" BY ADDING 4 TO POINTR.
818 006410 012711 166474      MOV      #HEADER!160000,(POINTR)      ;NOW INSERT THE DISJMP INSTRUCTION TO OUR HEADER
819 006414 012741 160000      MOV      #DISJMP,-(POINTR)          ;AND IT'S ADDRESS (PUT THEM IN BACKWARDS).
820 006420 005041              CLR      -(POINTR)          ;MAKE AVAILABLE A NEW CHARACTER SPOT.
821
822 006422 000207              INSRTX: RTS      PC          ;FINALLY RETURN TO THE CALLER.
823
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828 006424 012737 001000 172000 GTBUSE: MOV      #BSTART,GT40PC      ;ON A BUS ERROR, WE MERELY RESTART THE GT40 AT
829

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:THE RTI FOR THIS ROUTINE
:IS THE FIRST WORD OF THE TABLE
:BELOW-IT SAVES A WORD!

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INITIALIZATION TABLE FOR THE SCROLLER

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SETUP: .WORD 2 ;INITIALIZE 2 WORDS.--ALSO RTI FROM ABOVE
        .WORD 330 ;STARTING AT LOCATION 330
        .WORD GTBASE!160000 ;FIRST WORD IS POINTER TO BUS ERROR ROUT
        .WORD 200 ;SECOND WORD IS NEW STATUS WORD ON INTERRUPT.

        .WORD 7 ;INITIALIZE THE END OF THE BUFFER TO
        .WORD BLIMIT-2 ;A CLEAR SPACE TO INSERT THE CHARACTER.
        .WORD 0 ;THIS IS THE "RUNNING" START. THIS IS
        .WORD DISJMP,HEADER!160000 ;FOLLOWED BY A DISJMP TO OUR HEADER BLOC
        .WORD DISJMP,BSTART ;AND THEN A DISJMP TO THE START OF THE BUFFER
        .WORD DISJMP,BLIMIT-NUMLIN-NUMLIN ;AND A DISJMP TO THE FIRST CHAR ON SCREE

        .WORD 1 ;FINALLY START THE GT40 GOING AT
        .WORD GT40PC ;THE POSITION INSTRUCTION IN THE
        .WORD HEADER!160000 ;HEADER BLOCK.

        .WORD 0 ;END OF INIT CODE

:
:
HEADER: .WORD 103334 ;ENABL CHAR MODE,BLINKING
        .WORD 177 ;A BLINKING BOX-RUB OUT!
        .WORD 116124 ;GO TO POINT MODE
        .WORD 171340 ;LOAD STATUS REGISTER
        .WORD 0,1352 ;POINT TO UPPER LEFT
        .WORD 103324 ;BACK TO CHAR MODE
        .WORD DISJMP,JMPADD-2 ;AND TO THE CHANGING JMP INST.

.SBTTL COMMUNICATIONS AND MISC. SUPPORT ROUTINES
.PAGE

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006432 000002
006434 000330
006436 166424
006440 000200

006442 000007
006444 006776
006446 000000
006450 160000 166474
006454 160000 001000
006460 160000 006700

006464 000001
006466 172000
006470 166474

006472 000000

006474 103334
006476 000177
006500 116124
006502 171340
006504 000000 001352
006510 103324
006512 160000 007010

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COMMUNICATIONS HANDLING ROUTINES

THE DL-11 HANDLER

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895 006516 105737 175610 GETDL: TSTB DL11IS ;CHECK THE HOST INPUT STATUS.
896 006522 100011 BPL GETDL1 ;HOST DID NOT SEND ANYTHING, YET.
897 006524 113700 175612 MOVB DL11IB,CHAR ;HOST SENT US A CHARACTER. PROCESS IT.
898 006530 012737 000007 175610 MOV #7,DL11IS ;REENABLE THE HOST TELECOMMUNICATIONS.
899 006536 042700 177600 BIC #-200,CHAR ;MAKE CHARACTER JUST SEVEN BITS.
900 006542 001765 BEQ GETDL ;IF NULL, IGNORE IT.
901 006544 000207 RTS PC ;ELSE RETURN NOW.

903 006546 105737 177560 GETDL1: TSTB KBDIS ;DID USER TYPE A CHARACTER?
904 006552 100361 BPL GETDL ;NO. GO BACK AND CHECK HOST MACHINE.
905 006554 113737 177562 175616 MOVB KBDIB,DL110B ;MOVE THE CHARACTER TO THE HOST.
906 006562 000755 BR GETDL ;AND CHECK AGAIN FOR INPUT.
  
```

THE "GET CHARACTER" ROUTINE

```

917 006564 004737 166516 GETCHR: JSR PC,GETDL!160000 ;GET A CHARACTER FROM THE HOST NOW.
918 006570 020027 000175 CMP CHAR,#ALTMOD ;IS IT AN "ALTMODE"
919 006574 001025 BNE GETEXT ;NO. EXIT NOW.

921 006576 004737 166516 JSR PC,GETDL!160000 ;YES. GET ANOTHER ONE NOW.
922 006602 020027 000114 CMP CHAR,#'L ;IS IT AN "L"
923 006606 001501 BEQ LOADER ;YES. START LOADING NOW.
924 006610 020027 000122 CMP CHAR,#'R ;IS IT AN "R"
925 006614 001015 BNE GETEXT ;NO. IGNORE THE ALTMODE AND JUST RETURN THE CHAR

927 006616 012737 173000 007010 MOV #DISTOP,JMPADD-2 ;YES. RESET. STOP DISPLAY BY INSERTING A "DISTOP
928 006624 000137 166060 PRESTR: JMP RESTR!160000 ;INSTRUCTION IN THE BUFFER, AND RESTART.
  
```

THE "GET A SIX BIT CHARACTER" ROUTINE

```

935 : -----
936 :
937 :
938 :
939 006630 004737 166564 GETSIX: JSR PC,GETCHR!160000 ;GET A CHARACTER NOW.
940 006634 020027 000040 CMP CHAR,#40 ;IS IT A LEGAL PRINTING CHARACTER?
941 006640 002517 BLT L.BAD ;NOPE. ABORT
942 006642 020027 000137 CMP CHAR,#137 ;IT'S BIG ENOUGH. IS IT TOO BIG?
943 006646 003114 BGT L.BAD ;YEP. ABORT.
944 :
945 006650 000207 GETEXT: RTS PC ;RETURN TO THE CALLER.
946 :
947 :
948 : THIS OUTPUTS TWO CHARACTERS VIA A
949 : JSR SCAN,OUTLIT
950 : 'TWO CHARACTERS'
951 :
952 006652 112337 175616 OUTLIT: MOVB (SCAN)+,DL110B
953 006656 112337 175616 MOVB (SCAN)+,DL110B ;DOUBLE BUFFERED
954 006662 000203 RTS SCAN ;RETURN
955 :
956 :
957 :
958 :
959 :
960 :
961 :
962 : THE "GET AN EIGHT BIT CHARACTER" ROUTINE
963 : -----
964 :
965 :
966 :
967 : THIS ROUTINE DIFFERS FROM THE PREVIOUS ROUTINES
968 : IN THAT IT WILL TAKE SIX BIT CHARACTERS AND ASSEMBLE
969 : THEM FOR THE LOADER TO USE. NOTE THAT FROM THIS POINT
970 : ON WE WILL SWITCH TO THE LOADER DEFINITIONS OF THE
971 : REGISTERS. THUS THE CHARACTER IS RETURNED IN
972 : REGISTER "L.BYT" RATHER THAN CHAR (THOUGH THEY ARE
973 : PHYSICALLY THE SAME).
974 :
975 :
976 :
977 006664 004737 166630 GET8: JSR PC,GETSIX!160000 ;GET A SIXBIT CHARACTER.
978 006670 010046 MOV L.BYT,-(SP) ;SAVE IT ON THE STACK.
979 006672 005723 TST (INDEX)+ ;UPDATE INDEX TO NEXT ITEM (ALL ARE *2)
980 006674 000163 166676 JMP GET8TB-2!160000(INDEX) ;AND DISPATCH ACCORDING TO THE INDEX.
981 :
982 006700 000404 GET8TB: BR GET81 ;INDEX=2: ASSEMBLE FIRST CHAR
983 006702 000416 BR GET82 ;INDEX=4: ASSEMBLE SECOND CHAR
984 006704 000432 BR GET83 ;INDEX=6: ASSEMBLE THIRD AND LAST CHAR
985 : ;INDEX=8: RESET INDEX TO 0 [2] AND RETRY.
986 :
987 :
988 006706 012703 000002 GET84: MOV #2,INDEX ;THE FOURTH INDEX IS THE SAME AS THE FIRST
989 : ;INDEX. JUST RESET IT AND FALL THROUGH.
990 :

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K02

SCROLLING ROM BOOTSTRAP FOR THE GT40
 DDGTDG.P11 15-SEP-76 00:00

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 COMMUNICATIONS AND MISC. SUPPORT ROUTINES

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991
992 006712 004737 166630      GETB1: JSR      PC,GETSIX!160000      ;GET ANOTHER CHARACTER NOW.
993 006716 010004              MOV      L.BYT,HOLD      ;AND PRESERVE IT FOR NEXT TIME THRUJGH.
994 006720 006300              ASL      L.BYT          ;NOW THROW AWAY LEFT MOST BITS OF
995 006722 006300              ASL      L.BYT          ;THE 8 BIT CHARACTER. NOW MERGE IN
996 006724 106300              ASLB     L.BYT          ;THE LEFT TWO BITS OF THE
997 006726 106116              ROLB     (SP)           ;NEW SIX BIT CHARACTER WITH THE SIX
998 006730 106300              ASLB     L.BYT          ;BITS FROM THE CHARACTER ON THE
999 006732 106116              ROLB     (SP)           ;STACK. 1ST CHARACTER IS NOW ASSEMBLED,
1000 006734 012600              MOV      (SP)+,L.BYT    ;SO WE'LL RETURN IT TO THE USER.
1001 006736 000207              RTS      PC             ;AND THEN WE SHALL RETURN TO HIM.
1002
1003
1004 006740 006300      GETB2: ASL      L.BYT          ;THE SECOND CHARACTER IS CREATED FROM
1005 006742 006300              ASL      L.BYT          ;THE 4 RIGHT BITS OF THE PREVIOUS CHARACTER
1006 006744 106300              ASLB     L.BYT          ;AND THE FOUR MIDDLE BITS OF THE PRESENT
1007 006746 106104              ROLB     HOLD          ;8 BIT CHARACTER.
1008 006750 106300              ASLB     L.BYT          ;WE WILL CREATE THE NEW 8 BIT
1009 006752 106104              ROLB     HOLD          ;IN THIS REGISTER, SINCE IT
1010 006754 106300              ASLB     L.BYT          ;MORE CONVIENT. WE WILL MOVE OVER THE
1011 006756 106104              ROLB     HOLD          ;ANSWER AT THE END.
1012 006760 106300              ASLB     L.BYT          ;ONE MORE TO GO
1013 006762 106104              ROLB     HOLD          ;DONE.
1014 006764 010400              MOV      HOLD,L.BYT    ;BRING OVER THE VALUE.
1015 006766 012604              MOV      (SP)+,HOLD    ;AND REMEMBER THE LAST CHARACTER WE RECEIVED.
1016 006770 000207              RTS      PC             ;AND RETURN TO THE CALLER.
1017
1018
1019 006772 006100      GETB3: ROL      L.BYT          ;FINAL CHARACTER IS EASY. JUST A
1020 006774 106100              ROLB     L.BYT          ;SIMPLE MERGER OF LEFT TWO BITS OF
1021 006776 006004              ROR      HOLD          ;PREVIOUS VALUE WITH RIGHT SIX BITS
1022 007000 106000              RORB     L.BYT          ;OF LAST (4TH) CHARACTER RECEIVED.
1023 007002 006004              ROR      HOLD          ;
1024 007004 106000              RORB     L.BYT          ;AND WE ARE DONE.
1025 007006 005726              TST      (SP)+         ;FINALLY THROW AWAY STACK.
1026 007010 000207              RTS      PC             ;AND RETURN TO THE CALLER.
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.SBTTL THE LOADER
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1050 007012 012737 173000 007010  LOADER: MOV      #DISTOP, JMPADD-2      ;STOP THE GT40 BY INSERTING A "DISTOP" IN THE LI
1051
1052 007020 005003                      CLR      INDEX          ;RESET THE 8 BIT ASSEMBLER TO THE FIRST CHAR
1053
1054
1055 007022 005005                      L.LD2:  CLR      L.CKSM          ;CLEAR THE CHECKSUM
1056 007024 004737 167114              JSR      PC, L.PTR!160000      ;GET A BYTE NOW.
1057 007030 105300                      DECB     L.BYT              ;IS IT A ONE (HEADER)?
1058 007032 001373                      BNE     L.LD2              ;NO. WAIT FOR THE ONE.
1059
1060 007034 004737 167114              JSR      PC, L.PTR!160000      ;YES. SKIP OVER THE NEXT CHARACTER NOW.
1061
1062 007040 004737 167126              JSR      PC, L.GWRD!160000      ;ASSEMBLE A WORD NOW.
1063 007044 010002                      MOV      L.BYT, L.BC          ;MOVE OVER TO THE COUNTER.
1064 007046 162702 000004              SUB      #4, L.BC            ;REDUCE TO ACTUAL DATA COUNT.
1065 007052 022702 000002              CMP      #2, L.BC            ;ANY DATA AT ALL?
1066 007056 001433                      BEQ     L.JMP              ;NO. MUST BE END
1067 007060 004737 167126              JSR      PC, L.GWRD!160000      ;YES. ASSEMBLE A DATA WORD NOW.
1068 007064 010001                      MOV      L.BYT, L.ADR          ;AND THIS MUST BE THE FIRST ADDRESS.
1069
1070
1071 007066 004737 167114              L.LD3:  JSR      PC, L.PTR!160000 ;GET A BYTE OF DATA NOW.
1072 007072 002006                      BGE     L.LD4              ;ALL DONE?
1073 007074 105705                      TSTB    L.CKSM              ;YEP. COUNTER IS MINUS. CHECK CHECKSUM.
1074 007076 001751                      BEQ     L.LD2              ;CHECKSUM GOOD. GET NEXT COMMAND.
1075
1076
1077 007100 004337 166652              L.BAD:  JSR      SCAN, OUTLIT!160000 ;BAD LOAD INFORM HOST
1078 007104          175          102          .BYTE    ALTMOD, 'B          ;SEND ALTMODE B
1079 007106 000646                      BR      PRESTR              ;AND RESTART THE DISPLAY.
1080
1081
1082 007110 110021                      L.LD4:  MOVB     L.BYT, (L.ADR)+ ;INSERT BYTE INTO MEMORY.
1083 007112 000765                      BR      L.LD3              ;AND GET THE NEXT BYTE.
1084
1085
1086
1087 007114 004737 166664              L.PTR:  JSR      PC, GET8!160000 ;ASSEMBLE AN 8 BIT CHARACTER NOW.
1088 007120 060005                      ADD     L.BYT, L.CKSM          ;UPDATE THE CHECKSUM NOW.
1089 007122 005302                      DEC     L.BC                ;DECREMENT THE CHARACTER COUNTER.
1090 007124 000207                      RTS     PC                  ;AND RETURN TO THE CALLER NOW.
1091
1092
1093
1094 007126 004737 167114              L.GWRD: JSR      PC, L.PTR!160000 ;ASSEMBLE A WORD. FIRST GET A CHARACTER
1095 007132 010046                      MOV     L.BYT, -(SP)          ;AND SAVE IT.
1096 007134 004737 167114              JSR     PC, L.PTR!160000      ;AND THEN GET ANOTHER ONE.

```

SCROLLING ROM BOOTSTRAP FOR THE GT40
DDGTDC.P11 15-SEP-76 00:00

MACY11 27(1006) 05-NOV-76 12:17 PAGE 26
THE LOADER

```

1097 007140 000300          SWAB  L.BYT          ;AND THEN REASSEMBLE THE MESS.
1098 007142 052600          BIS   (SP)+,L.BYT      ;WITH THE FEARSOME POWER OF THE 11.
1099 007144 000207          RTS   PC              ;AND RETURN TO THE CALLER.
1100
1101
1102
1103
1104 007146 004737 167126    L.JMP: JSR  PC,L.GWRD!160000      ;ALL DONE WITH THE LOAD. ASSEMBLE
1105 007152 010046          MOV   L.BYT,-(SP)          ;THE STARTING ADDRESS NOW.
1106 007154 004737 167114    JSR  PC,L.PTR!160000        ;AND DON'T FORGET TO CHECKSUM IT.
1107 007160 105705          TSTB L.CKSM
1108 007162 001346          BNE  L.BAD                ;A BAD CHECKSUM. ALL IS EVIL.
1109
1110 007164 004337 166652    JSR  SCAN,OUTLIT!160000     ;GOOD CHKSUM, INFORM HOST
1111 007170      175      107    .BYTE  ALTMOD,'G          ;WITH ALTMOD G
1112
1113 007172 032716 000001    BIT  #1,(SP)              ;DO WE WANT TO START EXECUTION?
1114 007176 001401          BEQ  L.JMP1              ;YES. AWAY WE GO.
1115
1116 007200 000000          L.HALT: HALT              ;IF NOT, HALT.
1117
1118 007202 000136          L.JMP1: JMP  @ (SP)+       ;IF GO, THEN GO ALREADY. WHEEEEE!
1119
1120
1121
1122          .SBTTL  THE SELF TEST
1123
1124
1125
1126
1127
1128
1129
1130
1131
1132
1133
1134
1135          .PAGE

```

```

1136
1137
1138
1139
1140      100000      CHAR=100000
1141      104000      SHORTV=104000
1142      110000      LONGV=110000
1143      114000      POINT=114000
1144      120000      GRAPHX=120000
1145      124000      GRAPHY=124000
1146      130000      RELATV=130000
1147
1148      002000      INTO=2000
1149      002200      INT1=2200
1150      002400      INT2=2400
1151      002600      INT3=2600
1152      003000      INT4=3000
1153      003200      INT5=3200
1154      003400      INT6=3400
1155      003600      INT7=3600
1156
1157      000100      LPOFF=100
1158      000140      LPON=140
1159      000020      BLKOFF=20
1160      000030      BLKON=30
1161
1162      000004      LINE0=4
1163      000005      LINE1=5
1164      000006      LINE2=6
1165      000007      LINE3=7
1166
1167      160000      DJMP=160000
1168      164000      ONOP=164000
1169      170000      STATSA=170000
1170      173400      DSTOP=173400
1171
1172      000300      LPLITE=300
1173      000200      LPDARK=200
1174      000040      ITALO=40
1175      000060      ITAL1=60
1176      000004      SYNON=4
1177
1178
1179      174000      STATSB=174000
1180
1181      000100      INCR=100
1182      040000      INTX=40000
1183      001777      MAXX=1777
1184      001377      MAXY=1377
1185      020000      MINUSX=20000
1186      020000      MINUSY=MINUSX
1187      017600      MAXSX=17600
1188      000077      MAXSY=77
1189      000100      MINSUY=100
1190
1191

```

```

;THIS IS GT40 QUICK TEST
;GIVES QUICK VISUAL TEST
;OF CONDITION OF MACHINE
;WITHOUT READING IN DIAG.

```

```

;BRIGHTEST

```

```

;STOP INTERRUPT

```

```

;ITALICS OFF
;" ON
;SYNC ON

```

```

;LOAD GRAPH INCR
;INTENSIFY BIT
;BIGGEST X VECTOR
;BIGGEST Y VECTOR
;THE MINUS BIT
;BIGGEST X IN SHORTVEC
;" Y IN "
;MINUS BIT FOR Y IN SHORTVEC

```

```

1192 007204 012737 167214 172000 MOV @FILED!160000,GT40PC ;START THE GT4C
1193 007212 000001 WAIT ;AND WAIT
1194 007214 114020 FILED: POINT!BLKOFF ;POINT--INVISIBLE
1195 007216 000000 0
1196 007220 001377 MAXY
1197 007222 112004 LONGV!INT0!LINE0 ;DRAW TOP LINE
1198 007224 041777 INTX!MAXX
1199 007226 000000 0
1200 007230 112405 LONGV!INT2!LINE1
1201 007232 040000 INTX ;DRAW LINE TO RIGHT
1202 007234 021377 MINUSX!MAXY
1203 007236 113006 LONGV!INT4!LINE2
1204 007240 061777 INTX!MINUSX!MAXX ;DRAW BOTTOM LINE
1205 007242 000000 0
1206 007244 113407 LONGV!INT6!LINE3
1207 007246 040000 INTX
1208 007250 001377 MAXY ;DRAW LINE TO LEFT
1209 007252 114000 POINT
1210 007254 000400 400
1211 007256 000500 500
1212 007260 106200 SHORTV!INT1
1213 007262 057677 57677 ;+X+Y
1214 007264 106600 SHORTV!INT3
1215 007266 077677 77677 ;+X-Y
1216 007270 107200 SHORTV!INT5
1217 007272 077777 77777 ;-X-Y
1218 007274 107600 SHORTV!INT7
1219 007276 057777 57777 ;-X+Y
1220 007300 114000 POINT
1221 007302 001400 1400
1222 007304 000500 500
1223 007306 133000 RELATV!INT4!BLKON
1224 007310 057677 57677 ;+X+Y
1225 007312 077677 77677 ;+X-Y
1226 007314 077777 77777 ;-X-Y
1227 007316 057777 57777 ;-X+Y
1228 007320 114000 POINT
1229 007322 000400 400
1230 007324 000100 100
1231 007326 174120 STATSB!INCR+20 ;TRY GRAPH MODES
1232 007330 114000 POINT
1233 007332 001000 1000
1234 007334 000200 200
1235 007336 120000 GRAPHX
1236 007340 001010 !010
1237 007342 001020 !020
1238 007344 001030 !030

```

SCROLLING ROM BOOTSTRAP FOR THE 6740
007DC.P11 15-SEP-76 00:00

MACY1: 27(1006) 05-NOV-76 12:17 PAGE 29
THE SELF TEST

12748	007346	001040
12749	007350	001050
12750		
12751	007352	114000
12752	007354	001000
12753	007356	001200
12754		
12755	007360	124000
12756	007362	001020
12757	007364	001030
12758	007366	001040
12759	007370	001050
12760	007372	001060
12761		
12762	007374	160000
12763	007376	167214
12764		
12765		

1040
1050

POINT
1000
1200

GRAPHY
1020
1030
1040
1050
1060

DJMP
FILED!160000

.SBTTL PAPER TAPE BOOT

```

1266
1267
1268
1269      177550
1270      177560
1271
1272
1273
1274      007400      012701      160000
1275      007404      012702      000004
1276      007410      012703      167500
1277      007414      010712
1278      007416      012706      000024
1279      007422      014304
1280      007424      005714
1281      007426      100775
1282      007430      010712
1283      007432      012706      000024
1284      007436      010441
1285      007440      040601
1286      007442      010111
1287      007444      011102
1288      007446      005214
1289      007450      105714
1290      007452      100376
1291      007454      116412      000002
1292      007460      005211
1293      007462      120227      000375
1294      007466      001366
1295      007470      105222
1296      007472      000142
1297
1298
1299
1300      007474      177560
1301      007476      177550
1302
1303
1304
1305

```

```

: PAPER TAPE BOOT
HSR=177550 :HIGH SPEED READER ADDRESS
LSR=177560 :LOW SPEED READER ADDRESS
:
:   =ORIGIN+1400
PTBOOT: MOV   #160000,R1 :SET MEMORY CHECK LIMITS
        MOV   #4,R2      :TRAP ADDRESS IS LOC. 4
        MOV   #DEV+4!160000,R3 :POINTER TO DEVICE ADDRESSES
        MOV   PC,R2      :PRESET TRAP ADDRESS IN LOC. 4
        MOV   #24,SP     :STACK SET UP AT SPECIAL ADDRESS
DEV1:   MOV   -(R3),R4   :GET DEVICE ADDRESS
        TST   R4        :CHECK AVAILABILITY OF DEVICE
        BMI  DEV1       :CHECK DEVICE FOR ERRORS
        MOV   PC,R2     :RESET TRAP ADDRESS AT LOC. 4
        MOV   #24,SP    :SPECIAL ADDRESS USED AS MASK LATER
        MOV   R4,-(R1)  :DO MEM CHK: READER STATUS ADDRESS
        :
        : IS MOVED
        : SET R1=X7752 MASK IN SP=24
        : STORE OWN ADDRESS IN POINTER
LOOP:   MOV   R1,R1     :
        MOV   R1,R2     :GET BYTE POINTER
        INC  R4        :ENABLE READER
        TSTB R4        :TEST DONE BIT
        BPL  -2        :WAIT UNTIL READY
        MOVB 2(R4),R2   :THEN PICK IT UP AND STORE IT
        INC  R1        :BUMP POINTER
        CMPB R2,#375   :STORED JUMP OFFSET?
        BNE  LOOP     :NOT YET
        INCB (R2)+    :YES, ALL DONE
        JMP  -(R2)    :GO EXECUTE AS BRANCH
:
: DEVICE ADDRESSES FOLLOW - DO NOT CHANGE THE ORDER
DEV:   LSR                     :LOW SPEED READER
       HSR                     :HIGH SPEED READER
:
.SBTTL CASSETTE BOOT

```


E03

SCROLLING ROM BOOTSTRAP FOR THE GT40
 DDGTOC.P11 15-SEP-76 00:00

MACY11 27(1006) 05-NOV-76 12:17 PAGE 31
 CASSETTE BOOT

```

1304
1305
1306
1307      177500
1308
1309      007500 012700 177500
1310      007504 005010
1311      007506 010701
1312      007510 062701 000052
1313      007514 012702 000375
1314      007520 112103
1315
1316      007522 112110
1317      007524 100413
1318      007526 130310
1319      007530 001776
1320      007532 105202
1321      007534 100772
1322      007536 116012 000002
1323      007542 120337 000000
1324      007546 001767
1325      007550 000000
1326      007552 000755
1327
1328      007554 005710
1329      007556 100774
1330      007560 005007
1331
1332      007562 017640
1333
1334      007564 002415
1335
1336      007566 112024
1337
1338      007570 000000 000000
1339      007574 167500
1340      007576 000340
1341
1342
1343

: CASSETTE BOOT
:
: TACS=177500 ;TACS=177500 ;TA-11 CONTROL AND STATUS REGISTER
: =ORIGIN+1500
TABOOT: MOV #TACS,R0
: SELECT UNIT #0
CLR (R0) ;USE FOR PIC
RES: MOV PC,R1 ;R1 HOLDS ADDR. OF COMMAND TABLE
ADD #TABLE-,R1 ;MEMORY PTR. AND DATA FLAG
MOV #375,R2 ;TEST BITS
MOVB (R1)+,R3

LOOP1: MOVB (R1)+,(R0) ;COMMAND FROM TABLE TO TACS
BMI DONE ;WHEN COMMAND CODE NEG., QUIT
LOOP2: BITB R3,(R0) ;TEST READY AND T-REG BITS IN TACS
BEQ LOOP2 ;LOOP 'TIL SOMETHING COMES UP
INCB R2 ;ADVANCE MEMORY POINTER
BMI LOOP1 ;IF MINUS, TRY NEXT COMMAND
MOVB 2(R0),R2 ;READ DATA INTO MEMORY
CMPB R3,#0 ;FIRST BYTE READ SHOULD BE '240'
BEQ LOOP2 ;IF O.K., GO READ ANOTHER BYTE
STOP: HALT ;HALT ON ERROR
BR RES ;RESTART ON CONTINUE

DONE: TST (R0) ;CHECK FOR ERROR
BMI STOP ;HALT ON ERROR
CLR PC ;= 'JMP #0'

TABLE: .WORD 17640 ;.BYTE 240: READY+T-REG.
; .BYTE 37: ILBS+READY+GO
; .BYTE 15: SFB+GO
; .BYTE 5: READ+GO
; .BYTE 24: READ+ILBS
; .BYTE 224: READ+ILBS+E.O.TABLE
; THESE ARE FILLER WORDS
; POWER UP VECTOR AND PRIORITY

.SBTTL MR11-DB BOOT

```

:MR11-DB BULK STORAGE PROGRAM LOADER LISTING

1344
1345
1346
1347
1348 007600 010702
1349 007602 000451
1350 007604 177462
1351 007606 000005
1352
1353 007610 010702
1354 007612 000445
1355 007614 177406
1356 007616 000005
1357
1358
1359 007620 010702
1360 007622 000417
1361 007624 177344
1362 007626 000005
1363 007630 004003
1364 007632 100000
1365 007634 024000
1366
1367
1368 007636 010702
1369 007640 000410
1370 007642 172524
1371 007644 060003
1372 007646 060011
1373 007650 000200
1374 007652 100000
1375
1376
1377 007654 010702
1378 007656 000423
1379 007660 176716
1380
1381
1382 007662 000005
1383 007664 010200
1384 007666 005720
1385 007670 012001
1386 007672 005311
1387 007674 005720
1388 007676 012041
1389 007700 031011
1390 007702 001776
1391 007704 005720
1392 007706 031041
1393 007710 001406
1394 007712 000112
1395
1396
1397 007714 167600
1398 007716 000340
1399

```

:                .=ORIGIN+1600                ;KEEP TRACK OF ORIGIN
RF11:  MOV PC,R2                ;FIXED HEAD DISK (256 KW)
      BR OTHER
      177462
      5
RK11:  MOV PC,R2                ;MOVING HEAD DISK (CARTRIDGE)
      BR OTHER
      177406
      5
TC11:  MOV PC,R2
      BR TAPES
      177344                ;ADDRESS OF WORD COUNT
      5                    ;LAST COMMAND
      4003                 ;FIRST COMMAND
      100000               ;DONE MASK
      24000                ;ERROR MASK
TM11:  MOV PC,R2
      BR TAPES
      172524                ;ADDRESS OF BYTE COUNT
      60003                 ;LAST COMMAND
      60011                 ;FIRST COMMAND
      200                   ;DONE MASK
      100000                ;ERROR MASK
RP11:  MOV PC,R2                ;MOVING HEAD DISK (PACK)
      BR OTHER
      176716
TAPES:  RESET
      MOV R2,R0                ;GET THE ADDRESS OF THE BRANCH
      TST (0)+                 ;RD TO POINT AT LAST COMMAND
      MOV (0)+,R1              ;GET THE WORD COUNT ADDRESS
      DEC (1)                  ;SET UP FOR ADVANCE 1 RECORD
      TST (0)+                 ;MOVE RD TO FIRST COMMAND
      MOV (0)+,-(1)            ;COMMAND WORD TO COMMAND REG.
      BIT (0),(1)              ;LOOK FOR DONE INDICATORS
      BEQ ,-2                  ;NONE SET, TRY AGAIN
      TST (0)+                 ;DONE FIRST COMMAND, CHECK FOR ERROR
      BIT (0),-(1)             ;LOOK FOR SET ERROR BITS
      BEQ OTHER                ;NO ERRORS - TRY THE READ
AGAIN:  JMP (2)                ;RERUN FOR ERRORS
RFVEC:  RF11!160000            ;RF11 POWER UP VECTOR
      340

```

```

1400 007720 010702
1401 007722 000401
1402 007724 177450
1403
1404
1405 007726 000005
1406 007730 010200
1407 007732 005720
1408 007734 012001
1409 007736 012711 177000
1410 007742 011041
1411 007744 032711 100200
1412 007750 001775
1413 007752 100757
1414 007754 005007
1415
1416 007756 000000
1417 007760 167610
1418 007762 000340
1419 007764 167720
1420 007766 000340
1421 007770 167654
1422 007772 000340
1423 007774 167620
1424 007776 000340
1425
1426
1427

```

```

RC11:  MOV PC,R2          ;FIXED HEAD DISK (64K)
        BR  OTHER
        177450          ;ADRS OF WORD COUNT (COMMAND-2
                        ;COMMAND WORD (5) IS THE RESET

OTHER:  RESET
        MOV R2,R0       ;R0 TO POINT AT WORD COUNT ADRS
        TST (0)+        ;POINT TO ADDRESS
        MOV (0)+,R1     ;WORD COUNT ADDRESS TO R1
        MOV #-1000,(1)  ;LOAD WORD COUNT
        MOV (0)-,(1)    ;COMMAND TO COMMAND REGISTER
        BIT #100200,(1) ;CHECK FOR ERROR OR DONE
        BEQ  -4         ;IF NEITHER, KEEP LOOKING
        BMI AGAIN      ;ERROR, TRY AGAIN
        CLR PC

RKVEC:  0                ;FILLER
        RK11!160000    ;RK POWER UP VECTOR
        340

RCVEC:  RC11!160000    ;RC POWER UP VECTOR
        340

RPVEC:  RP11!160000    ;RP POWER UP VECTOR
        340

TCVEC:  TC11!160000    ;TC11 POWER UP VECTOR
        340

.SBTTL  ROM VERSION 1 VALUES
.PAGE

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1468
1469
1470
1471
1472
1473
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1479
1480
1481
1482
1483

000000
000001
000002
000003
000004
000005
000006
000007

000006
000007

000000
000001
000002
000003
000004
000005

000003
000000
000005
000001

.DSABL AMA
:DATA PATTERN STORED IN THE GT40 BOOTSTRAP VERSION 1
: ***** THIS IS A IMAGE LISTING OF THE GT40 (VT40) BOOTSTRAP *****
: THE DATA IS A MIRROR IMAGE OF THAT IN THE BOOTSTRAP ROMS
: ONLY THE ADDRESS FIELD IS CHANGED
:BOOTVT.S09 5/2/72 (SPECIAL)

: VT-40 BOOTSTRAP LOADER, VERSION S09, RELEASE R01, 5/2/72
: COPYRIGHT 1972, DIGITAL EQUIPMENT CORPORATION.
: 146 MAIN STREET
: MAYNARD, MASSACHUSSETTS 01754

: WRITTEN BY JACK BURNES, SENIOR SYSTEMS ARCHITECT!

: THIS ROUTINE IS INTENDED TO BE LOADED IN THE ROM PORTION OF THE VT-40.

: REGISTER DEFINITIONS:
R0=%0
R1=%1
R2=%2
R3=%3
R4=%4
R5=%5
R6=%6
R7=%7

SP=R6
PC=R7

RET1=R0 ;RETURN OF VALUE REGISTER.
INP1=R1 ;ARGUMENT FOR CALLED FUNCTION
INP2=R2 ;SECOND ARGUMENT.
WORK1=R3 ;FIRST WORK REGISTER.
WORK2=R4 ;SECOND WORKING REGISTER.
SCR1=R5 ;SCRATCH REGISTER.

LCKSM=WORK1 ;OVERLAPPING DEFINITIONS FOR LOADER PORTION.
LBYT=RET1
LBC=SCR1
LADR=INP1

```

1484
1485      036000      COREND=36000      ;FIRST LOCATION OF NON-CORE.
1486      166000      ROMORG=166000     ;WHERE THE ROM PROGRAM SHOULD GO.
1487
1488      000000      STARTX=0        ;WHERE TO START DISPLAYING THE X POSITIONS.
1489      001360      STARTY=1360     ;WHERE TO START DISPLAYING THE Y.
1490
1491
1492      022000      VT40PC=172000-150000 ;VT40 PROGRAM COUNTER.
1493      027560      KBDIS=27560      ;TTY INPUT STATUS.
1494      025614      P100S=25614      ;PDP-10 OUTPUT STATUS.
1495      025610      P10IS=25610      ;PDP-10 INPUT STATUS.
1496
1497      027562      KBDIB=KBDIS+2     ;TTY INPUT BUFFER.
1498      025612      P10IB=P10IS+2    ;PDP-10 INPUT CHARACTER.
1499      025616      P10OB=P100S+2    ;PDP-10 OUTPUT BUFFER.
1500
1501
1502      045776      P100C=COREND-2+10000 ;CHARACTER TO BE SENT TO THE PDP-10
1503      045772      P10IC=P100C-4     ;INPUT CHARACTER FROM IO PLUS ONE SAVE CHARACTER
1504      015770      STKSRT=P10IC-2-30000 ;FIRST LOCATION OF STACK.
1505
1506
1507      160000      JMPDIS=160000     ;THE VT-40 DISPLAY JUMP INSTRUCTION.
1508
1509
1510      000024      PWRFAL=24        ;POWER FAIL RESTART LOCATION.
1511
1512
1513
1514
1515
1516
1517
1518
1519
1520
1521
1522      016000      ; .=16000
1523      ;           ; .=ROMORG      ;SET THE ORIGIN NOW!!!!
1524
1525
1526
1527
1528
1529
1530
1531 016000 012705 000026  STARTA: MOV      #PWRFAL+2, SCR1 ;PICK UP POINTER TO P.F. STATUS.
1532 016004 005015          CLR      @SCR1      ;CLEAR IT OUT TO BE SURE.
1533 016006 010745          MOV      PC, -(SCR1) ;SET UP THE RESTART LOCATION.
1534
1535 016010 000005          RESET      ;RESET THE BUS.
1536
1537 016012 012767 000007 007570  MOV      #7, P10IS ;INITIALIZE PDP-10 INPUT
1538 016020 012767 000001 011532  MOV      #1, KBDIS ;INITIALIZE TTY INPUT.
1539 016026 012767 000201 007560  MOV      #201, P100S ;INITIALIZE PDP-10 OUTPUT.
  
```

1540						
1541						
1542						
1543	016034	012706	015770	RSTPT:	MOV	#STKSRT, SP ;SET UP THE STACK NOW!
1544	016040	005001			CLR	LADR ;CLEAR ADDRESS POINTER.
1545	016042	012702	160000		MOV	#JMPDIS, INP2 ;PLACE A DISPLAY JUMP INSTRUCTION IN A REGISTER.
1546	016046	010221			MOV	INP2, (LADR)+ ;MOVE IT TO LOCATION 0.
1547	016050	012711	166756		MOV	#DISPRG+150000, (LADR) ;MOVE ADDRESS POINTER INTO 2.
1548	016054	012701	000030		MOV	#PWRPAL+4, LADR ;SET UP WHERE WE WILL STORE CHARACTERS.
1549	016060	005000			CLR	RET1 ;PREPARE TO INSERT A ZERO CHARACTER.
1550	016062	004767	000022		JSR	PC, DOCHAR ;INSERT IT NOW.
1551	016066	005067	003706		CLR	VT40PC ;CLEAR THE DISPLAY PROGRAM COUNTER AND START.
1552						
1553	016072	004767	000210	MAJOR:	JSR	PC, GTCHR ;GT A CHARACTER NOW.
1554	016076	000240			NOP	
1555	016100	000240			NOP	
1556	016102	000240			NOP	
1557	016104	012746	166072		MOV	#MAJOR+150000, -(SP) ;INSERT IN DISPLAY BUFFER NOW.
1558						
1559	016110	010105		DOCHAR:	MOV	LADR, SCR1 ;GT CURRENT BUFFER POSITION NOW.
1560	016112	022525			CMP	(SCR1)+, (SCR1)+ ;BYPASS CURRENT DISPLAY JUMP.
1561	016114	005025			CLR	(SCR1)+ ;CLEAR FUTURE ADDRESS FOR JUMP.
1562	016116	010225			MOV	INP2, (SCR1)+ ;STICK IN TEMPORARY JUMP WHILE WE REPLACE CURREN
1563	016120	005015			CLR	(SCR1) ;A DISPLAY JUMP TO ZERO.
1564	016122	005011			CLR	(LADR) ;NOW REPLACE CURRENT DISPLAY JUMP BY THE CHARACT
1565	016124	050021			BIS	RET1, (LADR)+ ;IT'S DONE THIS WAY TO WASTE 2 CYCLES.
1566	016126	010211			MOV	INP2, (LADR) ;TO AVOID TIMING PROBLEMS WITH THE VT40.
1567	016130	000207			RTS	PC ;AND FINALLY RETURN.
1568						
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1581						
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1583						
1584	016132	004767	000124	GT8:	JSR	PC, GTSIX ;GT SIX BITS NOW.
1585	016136	010046			MOV	RET1, -(SP) ;SAVE THE CHARACTER NOW.
1586	016140	000401			BR	GTP84 ;BYPASS THE 8'ER
1587	016142	005002		GT84:	CLR	INP2 ;RESET THE MAGIC REGISTER NOW.
1588	016144	005722		GTP84:	TST	(INP2)+ ;INCREMENT WHERE TO GO.
1589	016146	066207	166250		ADD	GT8TB+150000(INP2), PC ;UPDATE PC NOW.
1590						
1591		016152		GT8P=.		
1592						
1593	016152	004767	000104	GT81:	JSR	PC, GTSIX ;GT A CHARACTER NOW.
1594	016156	010004			MOV	RET1, WORK2 ;SAVE FOR A SECOND.
1595	016160	006300			ASL	RET1

1596	016162	006300		ASL	RET1	:SHIFT TO LEFT OF BYTE
1597	016164	106300		ASLB	RET1	
1598	016166	106116		ROLB	2SP	:PACK THEM IN.
1599	016170	106300		ASLB	RET1	
1600	016172	106116		ROLB	2SP	:A GOOD 8 BIT THING.
1601	016174	012600		MOV	(SP)+,RET1	;POP AND RETURN NOW.
1602	016176	000207		RTS	PC	
1603						
1604	016200	006300	GT82:	ASL	RET1	;WORST CASE. SHIFT 4
1605	016202	006300		ASL	RET1	
1606	016204	106300		ASLB	RET1	
1607	016206	106104		ROLB	WORK2	
1608	016210	106300		ASLB	RET1	
1609	016212	106104		ROLB	WORK2	
1610	016214	106300		ASLB	RET1	
1611	016216	106104		ROLB	WORK2	
1612	016220	106300		ASLB	RET1	
1613	016222	106104		ROLB	WORK2	
1614	016224	010400		MOV	WORK2,RET1	
1615	016226	012604		MOV	(SP)+,WORK2	
1616	016230	000207		RTS	PC	
1617						
1618	016232	006100	GT83:	ROL	RET1	
1619	016234	006100		ROL	RET1	
1620	016236	006004		ROR	WORK2	
1621	016240	106000		RORB	RET1	
1622	016242	006004		ROR	WORK2	
1623	016244	106000		RORB	RET1	:FINAL CHARACTER ASSEMBLED.
1624	016246	005726		TST	(SP)+	;FUDGE STACK.
1625	016250	000207		RTS	PC	;AND RETURN NOW.
1626						
1627		016250	GT8TB	=	.-2	:PUSH ZERO CONDITION BACK INTO NEVER-NEVER LAND.
1628						
1629	016252	000000		.WORD	GT81-GT8P	
1630	016254	000026		.WORD	GT82-GT8P	
1631	016256	000060		.WORD	GT83-GT8P	
1632	016260	177770		.WORD	GT84-GT8P	
1633						
1634						
1635	016262	004767	000020	GTSIX:	JSR	PC,GTCHR
1636	016266	020027	000040		CMP	RET1,#40
1637	016272	002546			BLT	LBAD
1638	016274	020027	000137		CMP	RET1,#137
1639	016300	003143			BGT	LBAD
1640	016302	000207			RTS	PC
1641						
1642						
1643						
1644	016304	005726		GTCHP:	TST	(SP)+
1645						:UPDATE THE STACK.
1646	016306	012700	015772	GTCHR:	MOV	#P10IC-30000,RET1
1647	016312	004767	000064	GTCHL:	JSR	PC,CHECK
1648	016316	005710			TST	2RET1
1649	016320	001774			BEQ	GTCHL
1650	016322	011046			MOV	2RET1,-(SP)
1651	016324	005020			CLR	(RET1)+
						:SET UP POINTER TO THE INPUT CHARACTER.
						:ANY CHARACTERS THERE?
						:PUSH THE CHAR ON THE STACK.
						:CLEAR THE CHAR GOT FLAG NOW.

1652	016326	042716	177600		BIC	#-200,(SP)		:CLEAR AWAY PARITY NOW.
1653	016332	001764			BEQ	GTCHP		:IF ZERO, GT ANOTHER
1654	016334	022716	000177		CMP	#177,(SP)		
1655	016340	001761			BEQ	GTCHP		:ALSO IGNORE RUBOUTS.
1656	016342	022710	000175		CMP	#175,@RET1		:WAS IT A "175"
1657	016346	001007			BNE	GTNP		:NOPE.
1658	016350	011610			MOV	(SP),@RET1		:YEP. RESET IN CASE OF ABORT.
1659	016352	021027	000122		CMP	@RET1,#122		:IS IT AN R
1660	016356	001626			BEQ	RSTRT		:YEP. RESTART
1661	016360	021027	000114		CMP	@RET1,#114		:IS IT AN L
1662	016364	001455			BEQ	LOAD		:YEP. LOAD.
1663								
1664	016366	011610			GTNP: MOV	(SP),@RET1		:NOW DO THE FDUGING.
1665	016370	012500			MOV	(SP)+,RET1		
1666	016372	020027	000175		CMP	RET1,#175		
1667	016376	001743			BEQ	GTCHR		:IF ALTMODE, LOOP
1668	016400	000207			RTS	PC		
1669								
1670								
1671								
1672								
1673								
1674								
1675								
1676								
1677	016402	005767	027370		CHECK: TST	P100C		:DO WE WANT TO OUTPUT?
1678	016406	001410			BEQ	CHECK1		:NO.
1679	016410	105767	007200		TSTB	P100S		:WE DO. IS THE IO READY?
1680	016414	100005			BPL	CHECK1		:NOT QUITE.
1681	016416	016767	027354	007172	MOV	P100C,P100B		:IT'S READY. SEND THE CHARACTER.
1682	016424	005067	027346		CLR	P100C		:AND THE SAVED CHARACTER.
1683								
1684	016430	105767	011124		CHECK1: TSTB	KBDIS		:HEY, IS THE KEYBOARD READY?
1685	016434	100014			BPL	CHECK3		:NOPE. NO LUCK.
1686	016436	116746	011120		MOVB	KBDIB,-(SP)		:YEP. SAVE THE CHARACTER NOW.
1687	016442	012767	000001	011110	MOV	#1,KBDIS		:AND REENABLE THE COMMUNICATIONS DEVICE.
1688								
1689	016450	004767	177726		CHECK2: JSR	PC,CHECK		:IS THE OUTPUT READY?
1690	016454	005767	027316		TST	P100C		
1691	016460	001373			BNE	CHECK2		:IF NOT, WAIT TILL DONE.
1692	016462	012667	007130		MOV	(SP)+,P100B		:AND THEN SEND OUT THE CHARACTER.
1693								
1694								
1695	016466	105767	007116		CHECK3: TSTB	P10IS		:IS THE IO TALKING TO ME.
1696	016472	100011			BPL	CHECK4		:NOPE. EXIT.
1697	016474	116767	007112	027270	MOVB	P10IB,P10IC		:GT THE CHARACTER NOW.
1698	016502	052767	177400	027262	BIS	#-400,P10IC		:MAKE SURE IT'S NONE ZERO.
1699	016510	012767	000007	007072	MOV	#7,P10IS		:REINITIALIZE COMMUNICATION LINE.
1700								
1701	016516	000207			CHECK4: RTS	PC		:AND RETURN.
1702								
1703								
1704								
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1718 ; THE L O A D E R
1719
1720 016520 005002 LOAD: CLR INP2 ;RESET TO FIRST 8 BIT CHARACTER.
1721 016522 012712 172000 MOV #172000,(INP2) ;AND ALSO CLEVERLY STOP THE VT40.
1722 016526 012706 015770 MOV #STKSRT,SP ;RESET STACK POINTER NOW.
1723
1724 016532 005003 LLD2: CLR LCKSM ;CLEAR THE CHECKSUM
1725 016534 004767 000070 JSR PC,LPTR ;GT A BYTE NOW.
1726 016540 105300 DECB LBYT ;IS IT ONE?
1727 016542 001373 BNE LLD2 ;NOPE. WAIT AWHILE
1728 016544 004767 000060 JSR PC,LPTR ;YEP. GT NEXT CHARACTER.
1729
1730 016550 004767 000072 JSR PC,LGWRD ;GT A WORD.
1731 016554 010005 MOV LBYT,LBC ;GT THE COUNTER NOW.
1732 016556 162705 000004 SUB #4,LBC ;CHOP OFF EXTRA STUFF.
1733 016562 022705 000002 CMP #2,LBC ;NULL?
1734 016566 001437 BEQ LIMP ;YEP. MUST BE END.
1735 016570 004767 000052 JSR PC,LGWRD ;NOPE. GT THE ADDRESS.
1736 016574 010001 MOV LBYT,LADR ;AND REMEMBER FOR OLD TIMES SAKE.
1737
1738 016576 004767 000026 LLD3: JSR PC,LPTR ;GT A BYTE (DATA)
1739 016602 002010 BGE LLD4 ;ALL DONE WITH THE COUNTER?
1740 016604 105703 TSTB LCKSM ;YEP. GOOD CHECK SUM?
1741 016606 001751 BEQ LLD2 ;NOPE. LOAD ERROR.
1742
1743 016610 012700 LBAD: MOV (PC)+,RET1 ;SEND OUT SOME CHARACTERS NOW.
1744 ; .BYTE 175,102 ;"CTRL BAD"
1745 016612 102 175 ;.BYTE 102,175 ;"BAD CTRL"
1746 016614 004767 000110 JSR PC,SENDIT
1747 016620 000167 177210 JMP RSTRT
1748
1749 016624 110021 LLD4: MOVB LBYT,(LADR)+ ;PLACE THE BYTE IN CORE.
1750 016626 000763 BR LLD3 ;GT ANOTHER ONE.
1751
1752 016630 004767 177276 LPTR: JSR PC,GT8 ;GT 8 BITS NOW.
1753 016634 060003 ADD LBYT,LCKSM ;UPDATE CHECKSUM
1754 016636 042700 177400 BIC #177400,LBYT ;CLEAN UP THE BYTE NOW.
1755 016642 005305 DEC LBC ;UPDATE THE COUNTER.
1756 016644 000207 RTS PC ;RETURN NOW.
1757
1758 016646 004767 177756 LGWRD: JSR PC,LPTR ;GT A CHARACTER.
1759 016652 010046 MOV LBYT,-(SP) ;SAVE FOR A SECOND.
1760 016654 004767 177750 JSR PC,LPTR ;GT ANOTHER CHARACTER.
1761 016660 000300 SWAB LBYT ;NOW ASSEMBLE THE WORD.
1762 016662 052600 BIS (SP)+,LBYT ;AND RETURN WITH A 16 BITER.
1763 016664 000207 RTS PC

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1764
1765 016666 004767 177754      L JMP:   JSR      PC, LGWRD      ;GT A WORD
1766 016672 010046              MOV      LBYT, -(SP)          ;SAVE ON THE STACK.
1767 016674 004767 177730      JSR      PC, LPTR           ;GT A CHARACTER.
1768 016700 105703              TSTB     LCKSM              ;IS IT ZERO?
1769 016702 001342              BNE      LBAD              ;YEP. WHAT CRAP.
1770 016704 032716 000001      BIT      #1, (SP)          ;IS IT ODD?
1771 016710 001406              BEQ      L JMP1            ;YEP. START PROGRAM GOING NOW.
1772 016712 012700              MOV      (PC)+, RET1       ;TELL PDP-10 WE'VE LOADED OK.
1773
1774 016714      107      175      :      .BYTE 175, 107          ;"CTRL GOOD"
1775 016716 004767 000006      .BYTE 107, 175          ;"GOOD CTRL"
1776 016722 000000              JSR      PC, SENDIT
1777 016724 000776              HALT
1778
1779 016726 000136      L JMP1:  JMP      @ (SP)+      ;AND AWAY WE GO.
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1797 016730 004767 177446      SENDIT: JSR      PC, CHECK      ;POLL THE OUTPUT DEVICE NOW.
1798 016734 005767 027036      TST      P100C            ;OUTPUT CLEAR?
1799 016740 001373              BNE      SENDIT           ;NOPE. LOOP AWHILE LONGER.
1800 016742 010067 006650      MOV      RET1, P100B      ;SEND OUT THE CHARACTER.
1801 016746 105000              CLRB     RET1             ;CLEAR THE BYTE.
1802 016750 000300              SWAB     RET1             ;AND SWAP THEM NOW.
1803 016752 001366              BNE      SENDIT           ;IF NOT EQUAL. REPEAT.
1804 016754 000207              RTS      PC
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AGAIN	007712	1394#	1413						
ALTRAC=	000176	597#	918	1078	1111				
BELL	006250	738	770#						
BLIMIT=	007000	590#	593	683	704	789	808	855	859
BLKOFF=	000020	1159#	1195						
BLKON =	000030	1160#	1230						
BSTART=	001000	589#	791	811	828	858			
CHAR =	100000	540#							
CHARA	001020	177#	353#	354	360	376			
CHECK	016402	1647#	1677#	1689	1797				
CHECK1	016430	1678#	1680	1684#					
CHECK2	016450	1689#	1691						
CHECK3	016466	1685#	1695#						
CHECK4	016516	1696#	1701#						
COPEMO=	036000	1485#	1502						
COASTR=	000004	592#	652						
CP	006206	747#							
CRLF =	005015	596#	686						
DEV	007474	1275#	1300#						
DEV!	007422	1278#	1280						
DISTMP=	160000	599#	819	857	858	859	975		
DISPRG	016756	1547#	1830#						
DISTOP=	173000	600#	927	1050					
DJMP	= 160000	1157#	1262						
DL111B=	175612	578#	579	897					
DL111S=	175610	577#	578	645#	895	898#			
DL110B=	175616	580#	905#	952#	953#				
DL110S=	175614	579#	580	648#	666	668#			
DNOP	= 164000	1168#							
DOCHAR	016110	1550	1559#						
DONE	007554	1317	1328#						
DONE0	001422	291#							
DONE1	001474	298	305#						
JSR	001006	172#	290#						
DSTOP =	173400	1170#							
JSR =	177570	158#	162	443					
JUMP	001016	176#	195#	198#	214#	216#	217	221#	
DBTYP	002114	322#	326#	403#	408				
END	001410	289#							
ENOCOR	006036	655#	656						
ERROR1	001122	192	203#						
ERROR2	001170	219#							
ERRVEC=	000004	159#	441	442#	449#				
FF	006256	743	773#						
FFLOOP	006262	768	775#	778					
FILECNT	001012	174#	365#	370#					
FILED	007214	1192	1195#	1263					
FILLER	001010	173#	365						
GETCHR	006564	726	917#	939					
GETDL	006516	895#	900	904	906	917	921		
GETDL1	006546	896	903#						
GETEXT	006650	919	925	945#					
GETSIX	006630	939#	977	992					
GETB	006664	977#	1087						
GETBTB	006700	980	982#						
GETB!	006712	982	992#						

FL001	000000	1739							
FL002	000000	1727	1741						
FL003	000000	1750							
FL004	000000	1749							
FL005	000000	1720							
FL006	000000	1050							
FL007	000000	1199	1203	1207	1211				
FL008	000000	1294							
FL009	000000	1321							
FL010	000000	1319	1324						
FL011	000000	1728	1738	1752	1758	1760	1767		
FL012	000000	1300							
FL013	000000	943	1077	1108					
FL014	000000	1067	1094	1104					
FL015	000000	1104							
FL016	000000	1118							
FL017	000000	1058	1074						
FL018	000000	1083							
FL019	000000	1082							
FL020	000000	1056	1071	1087	1094	1096	1106		
FL021	016072	1553							
FL022	017600	1187							
FL023	000077	1188							
FL024	001777	1183	1200	1208					
FL025	001377	1184	1197	1205	1213				
FL026	000100	1189							
FL027	020000	1185	1186	1205	1208				
FL028	023000	1186							
FL029	002222	436	433						
FL030	002224	314	337	340	342	437			
FL031	002251	325	438						
FL032	006212	730	739	751					
FL033	006242	653	659						
FL034	000040	594	682	684	773	859			
FL035	006132	726	728	734	753	763	771	779	782
FL036	166000	575							
FL037	007726	1349	1354	1378	1393	1401	1405		
FL038	006652	649	952	1077	1110				
FL039	002116	323	327	404					
FL040	002146	413	425						
FL041	114000	1143	1195	1215	1227	1236	1240	1251	
FL042	006624	928	1079						
FL043	001034	184	305						
FL044	001040	185	186						
FL045	001500	166	310	344					
FL046	001536	318							
FL047	001554	322	338						
FL048	001572	326	334						
FL049	001642	332	339						
FL050	001024	165	179						
FL051	177776	157	396						

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SCROLLING ROM BOOTSTRAP FOR THE 3740
 DDGTDC.P11 15-SEP-76 00:00

MACY11 27,1006) 05-NOV-76 12:17 PAGE 46
 CROSS REFERENCE TABLE -- USER SYMBOLS

PTBOOT	007400	1273#						
PLRFAL=	000024	1510#	1531	1548	1836			
PICIB =	025612	1498#	1697					
PIDIC =	045772	1503#	1504	1646	1697*	1698*		
PIDIS =	025610	1495#	1498	1537*	1695	1699*		
P1008 =	025616	1499#	1681*	1692*	1800*			
P100C =	045776	1502#	1503	1677	1681	1682*	1690	1798
P100S =	025614	1494#	1499	1539*	1679			
RCVEC	007764	1419#						
RC11	007720	1400#	1419					
RELATV=	130000	1145#	1230					
RES	007506	1311#	1326					
RESTRT	006060	680#	928					
RE*JRN	002100	186*	390*	394#	397			
RVVEC	007714	1397#						
RF11	007600	1348#	1397					
RKVEC	007760	1417#						
RK11	007610	1353#	1417					
ROMA00	001000	169#	190	211	243	256*	275	318
ROMORG=	166000	1486#						
RPVEC	007770	1421#						
RF11	007654	1377#	1421					
RSTRT	016034	1543#	1660	1747				
SCOPE =	104400	154#	206	236	264	287		
SCOPEB	002102	382	388	395#				
SCOPEC	002024	151	381#					
SCOPEF	002076	385	387*	389*	393#			
SCOPEG	002062	384	386	389#				
SENDIT	016730	1746	1775	1797#	1799	1903		
SETDUN	006126	695	704#					
SETLP1	006074	686#	688					
SETLP2	006110	694#	701					
SETLP3	006116	698#	700					
SETLP	006432	691	849#					
SHORTV=	104000	1141#	1218	1220	1222	1224		
START	006000	171	453	644#				
STARTA	016000	456	1531#					
STARTX=	000000	1488#	1832					
STARTY=	001360	1489#	1833					
STATSA=	170000	1169#						
STA*SB=	174000	1179#	1239					
STKPTR=	000500	160#	179	185	242	310		
STKSRT=	015770	1504#	1543	1722				
STOP	007550	1325#	1329					
SWITCH	002256	180	312	441#				
SWP	000172	163#	381	383	443*	444	448*	450
SWREG	000170	162#	448					
SYNON =	000004	1176#						
*AB	006222	740	758#	762				
*ABLE	007562	1312	1332#					
TABOOT	007500	1309#	1339					
TACS =	177500	1307#	1309					
TAPES	007662	1360	1369	1382#				
TCVEC	007774	1423#						
TC11	007620	1359#	1423					
TERM	001022	178#	352*	354				

SCROLLING ROM BOOTSTRAP FOR THE G740
DOSTDC.P11 15-SEP-76 00:00

MACY11 27.1006) 05-NOV-76 12:17 PAGE 47
CROSS REFERENCE TABLE -- USER SYMBOLS

TPEND=	007776	591	646	680																
TH11	007636	1388																		
TPCSR	177564	1575	292	295	320	329	357	362	368	374	404	414	429*							
TPDBR	177566	1575	292*	295*	330*	364*	367*	372*	376*	416*										
TYPEN	001662	312	315	314	336	339	341	350												
TYPEN	001672	352	355	360																
TYPEN	001720	355	360																	
TYPEN	002006	361	374																	
TI	001052	190	201	205																
TI	001072	192	206																	
TI	001138	202	206																	
TI	001138	202	206																	
TI	001150	214	220	223																
TI	001150	214	220	223																
TI	001150	214	220	223																
TI	001206	221	221	224																
TI	001206	221	221	224																
TI	001206	221	221	224																
TI	001228	230	233	232																
TI	001228	230	233	232																
TI	001228	230	233	232																
TI	001228	230	233	232																
TI	001250	244	248																	
TI	001250	244	248																	
TI	001250	244	248																	
TI	001256	255	259																	
TI	001256	255	259																	
TI	001276	273	276	286																
TI	001276	273	276	286																
TI	001304	279	283																	
TI	001304	279	283																	
TI	001324	254	259																	
TI	001324	254	259																	
TI	001344	273	276	286																
TI	001344	273	276	286																
TI	001360	276	281	286																
TI	001360	276	281	286																
TI	001374	279	283																	
TI	001374	279	283																	
TI	001406	287																		
TI	001406	287																		
VT	006244	742	766																	
VT40PC=	022000	1492	1551*																	
WORDS	001002	170	191	212	244	285	317	452*	455*											
WORDS	017000	439	630	1290	1312	1390	1412	1522*	1591	1627	1777	358	363	369	375	415				

COMMEN	10
ENDCOM	10
ESCAPE	10
GETPRI	10
GETSWR	10
MULT	10
NEWTST	10
POP	10
PUSH	10
REPORT	10
SETPRI	10
SETUP	10
SKIP	10
SLASH	10
SARS	10
SWRSL	10
TYPBIN	10
TYPDEC	10
TYPNAM	10
TYPNUM	10
TYPCS	10
TYPOCT	10
TYPTXT	10
SSESCA	10
SSNEWT	10
SSSKIP	10
.EQUAT	10
.HEADE	10
.KTI1	10
.SETUP	10
.SWRHI	10
.SACT1	10
.SAPT8	10
.SAPTH	10
.SAPTY	10
.SASTA	10
.SCATC	10
.SCMTA	10
.SDB2D	10
.SDB2O	10
.SDIV	10
.SEOP	10
.SERRO	10
.SERRT	10
.SMULT	10
.SPOWE	10
.SRAND	10
.SRDDE	10
.SRDOC	10
.SREAO	10
.SR2AZ	10
.SSAVE	10
.SSB2D	10
.SSB2C	10
.SSCOP	10
.SSIZE	10

SCROLLING ROM BOOTSTRAP FOR THE GT40
DDGTDC.P11 15-SEP-76 00:00

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CROSS REFERENCE TABLE -- MACRO NAMES

.SSUPR 10
.STRAP 10
.STYPB 10
.STYPC 10
.STYPE 10
.STYPO 10
.S4QCA 10
.1170 10

.ABS. 017000 000

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

DDGTDC.BIN,DDGTDC.SEQ SOL/CRF/NL:TOC=DDGTDC.SML,DDGTDC.P11
RUN-TIME: 23 28 1 SECONDS
RUN-TIME RATIO: 189/53=3.5
CORE USED: 32K (63 PAGES)

